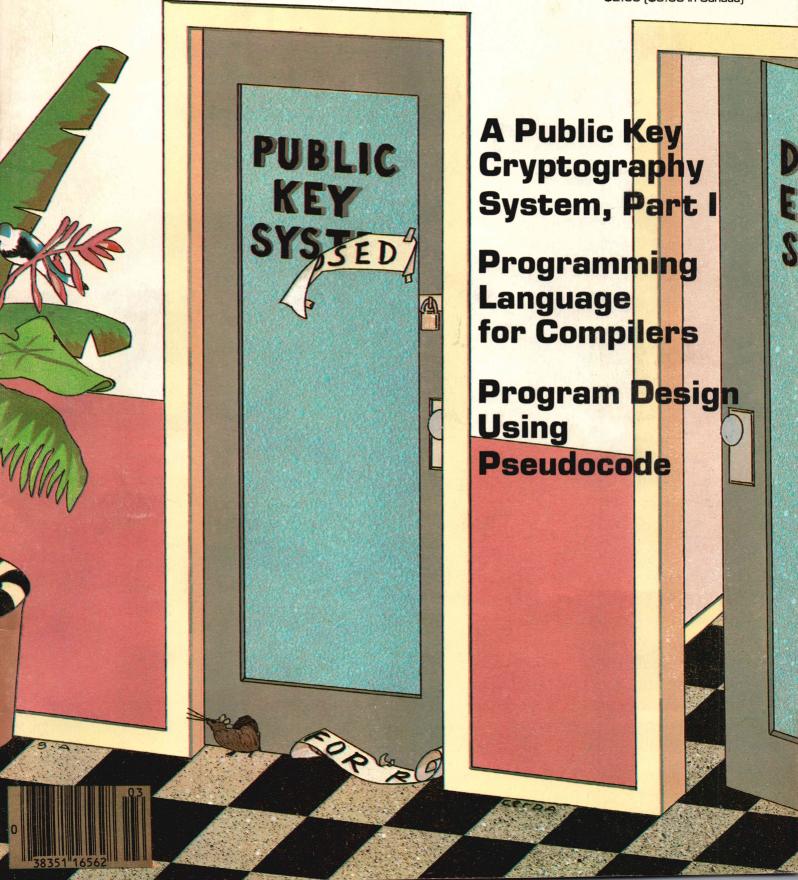
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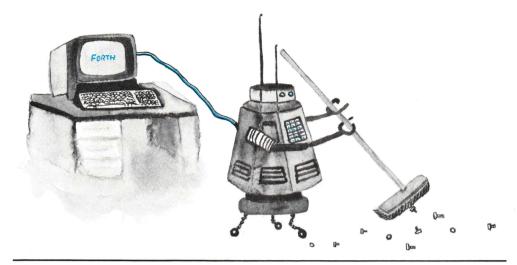


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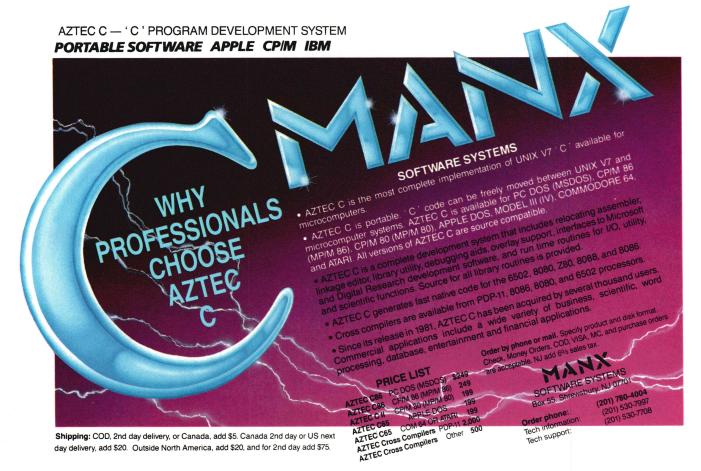


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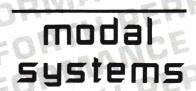
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For the Experienced in Microcomputing

March 1984 Volume 9, Issue 3

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EDITORIAL

Several people found the time interval between the formal announcement of the telecommunications issue in our pages and the copy deadline to be a bit tight. Let that not be said about our *next* special issue. Be advised that our annual Forth issue is again planned for September, and the copy deadline will be May 14, 1984. We will provide more details next month, but interested parties may contact us at P. O. Box E, Menlo Park, CA 94026; (415) 323-3111.

We have a correction and an elaboration regarding last month's issue. On page 44, a telephone number was provided for testing one's VPC implementation. Unfortunately, the number given was the main number for Unir, not the VPC test line. The correct number to dial to test your VPC is (317) 842-6986.

The sidebar which accompanied the communications protocols article noted that a Monte Carlo simulation was being set up to estimate some of the accuracies for various error-checking schemes. Author Leslie Brooks has provided a Letter to the Editor this month which details the outcome of the simulation, and its effect on their initial predictions.

Below you will find the list of our current referees. As with other aspects of *DDJ*, we assume that the board will evolve over time. While we intend to list the referees that work on each issue, we will also publish a complete list periodically.

Our appreciation to those who have all along been informally providing technical advice and insights. Our special thanks to David E. Cortesi for his continuous and substantial support, and to Kim Harris for his willingness to look at so many Forth articles over the years.

Reynold Wiggins

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I FTTERS



The editorial response card is a great way to talk to us, but don't forget that Dr. Dobb's Journal also welcomes letters to the editor as a forum for ideas, innovations, irascibility and even idiosyncrasies. Some letters may be edited for clarity and brevity. The Doctor likes hearing from you – keep on writing.

Method:

XOR

Sum

Sum w/carry

Random Errors: Clustered Errors: 1 in 136 1 in 417 1 in 187 1 in 381 1 in 221 1 in 360

Table
Probability of Missing an Error

Accuracy Update

Dear Doctor:

In our recent article on communications protocols (February 1984, DDJ No. 88), my friend John Rasp and I were discussing the relative merits of various methods of error detection. As we said there, the mathematics becomes very tricky if the changed bits are clustered together rather than randomly distributed. In order to get accurate results for the clustered errors, and to check our results for random errors, we finally ran a Monte Carlo simulation on the university's Cyber 760 mainframe. The results were very enlightening; although he couldn't prove it John had not expected the two cases to differ, but in fact they did. It turns out (as you can see by looking at the table top right) that all of the common methods of error detection are significantly better at catching errors if the changed bits are clustered together. In fact, the XOR method of calculating a checksum is the worst method of calculating the checksum if the errors are random, but the best method if the errors are clustered.

These results should be accurate to 3 digits. The simulation ran 40,000 iterations, generating a block of 128 bytes of random data each time, then randomly choosing the first bit to be clobbered, then randomly choosing the number of bits to be clobbered, then choosing the values of the clobbered bits. The number of bits to be hit was clustered around 10, in the usual bell curve. The simulation was written in Fortran, and required 15 minutes of CPU time to run (on a 10 megaflop machine)!

I hope this clears up any lingering questions.

Sincerely, Leslie Brooks Computing Center The Florida State University Tallahassee, FL 32306

(Continued on page 90)

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DR. DOBB'S CLINIC

by D.E. Cortesi, Resident Intern

Basically Precise

That describes the material sent us by Allan Behler, printed in the November Clinic. Allan had discovered that, in Microsoft BASIC-80, a constant assigned to a double-precision variable wasn't stored as expected:

> DEFDBL A A=134.12 PRINT A 134.1199951171875

The display could be made correct with PRINT USING, but repeated use of such values could accumulate significant errors. With a good deal of difficulty he worked his way to a solution using a rounding function to convert from a single-precision value to a double-precision one. Here is a corrected, improved version of the rounding function shown here in November:

10 DEF FNRD#(X#)=
 INT(X # *100+0.5)/100 #
20 DEFDBL A
30 A=134.12 : PRINT A
40 A=FNRD#(A) : PRINT A
RUN
134.1199951171875
134.12

Three things are needed to make the function work as expected. First, there are two expressions in FNRD# (one is the argument of INT, the other is the division), and at least one of the elements of each expression must be double precision. Why? Read the following rule from the BASIC-80 Reference Manual (Microsoft, 1979, page 1-8):

"During expression evaluation, all of the operands in an arithmetic or relational operation are converted to the same degree of precision, i.e., that of the most precise operand."

In other words, if the divisor isn't "100 #" indicating a double-precision constant, the result of the division will be single precision. The single-precision result will be stretched to double to satisfy the "#" in the name of the function, and the result will be exactly the same as assigning 134.12 to A in the first place.

Second, the function name has to indicate that it returns a double-precision result. If it doesn't — if its name is simply FNRD, for example — the double-precision result of the expression will be truncated to single precision to match the implicit type of the function. Then that result will be stretched to double precision

for the assignment to A, and we are back to square one.

All these problems are the effects of another rule (*Ibid*, page 1-9):

"If a double-precision variable is assigned a single-precision value, only the first seven digits, rounded, of the converted number will be valid. [Note: it's six digits in the IBM manual.] The absolute value of the difference between the printed [sic] double-precision number and the original single-precision value will be less than 6.3E-8 times the original"

It isn't clear to us why, after a constant has been converted correctly to a single-precision float value, copying it to a double-precision float value should change its low-order bits (if that's what happens). However, the effect is there, it's documented, and it's dangerous.

Finally, the function can still fail if it doesn't indicate that it requires a double-precision argument. If its dummy argument is named X rather than X#, and if the value of the argument has more than six significant digits, the result is wrong. The (effectively double-precision) argument gets truncated to single precision when assigned to the dummy variable, and data is lost.

Well, Allan's record of his explorations provoked some sharp replies. Let's read some of them. Charles Marshall says, "If the example is rewritten to use a double-precision constant, the correct answer is printed:

> A=134.12# : PRINT A 134.12

"Mr. Behler continues to use mixedmode arithmetic in the three program segments, which will tend to exaggerate the problem he is trying to solve. In addition his use of 0.51 as a rounding constant is invalid, resulting in, for example, the number 1.0049 being 'rounded' to 1.01."

Rounding is another whole topic. We think Marshall is right, but would anyone care to guess why Allan might have (very deliberately) used 0.51 instead?

Meanwhile, Joseph McDermott writes:

"People have been stumbling over this problem since 1977. I found the correct solution on my TRS-80 in 1978, and it still applies to the IBM PC. Why does not Microsoft explain it is their manuals and save everybody so much grief? Numeric precision problems disappear if a few simple rules are followed."

McDermott's Rule #1 is: All values entering a calculation must be double precision. You must be consistent from beginning to end of your program, no exceptions, he says. This extends to things like input where, if you read numeric input as a string and then convert it, you must remember to append the magic pound-sign:

INPUT NUM\$
NUM# = VAL(NUM\$ + "#")

"BASIC reverts to single precision at the slightest excuse," he says. "A doubleprecision variable on the left of the assignment statement is *not* sufficient to enforce double-precision calculations."

Joseph Sabin wrote in with what we think is a false lead: "Whenever you change a single-precision number to a double-precision number, you must use the double-precision exchange function:

A# = CDBL(123.12)

or you will get inaccurate results."

A good point, we thought, until we tried it:

A# = CDBL(134.12) PRINT A# 134.1199951171875

Oops. Sorry, Joseph, we can't find any difference between using CDBL and not. It is definitely not a replacement for the use of a double-precision marker on the constant itself — "134.12#" and "CDBL-(134.12)" do not appear to be the same thing. In fact, the documentation for CDBL reveals exactly that. Take this example, exactly as shown in the IBM BASIC manual (first edition, page 4-31):

10 A = 454.67 20 PRINT A; CDBL(A) RUN 454.67 454.6699829101563

That's Behler's original problem; CDBL does not do what FNRD# does. What FNRD# does is probably not useful when working with constants, the trailing pound sign does it better. Its only use would be in the rare cases when you have a computed value of money in a single-precision variable and want to assign it to a double-precision variable. In that specific case it does a better job that CDBL.

Transcendentally Precise

Richard Falk writes asking for help in finding, "either a machine code source listing or a fast algorithm for trig functions on the 6502. I've coded the power series for sin, cos, tan, arcsin, and arctan, but these require so many terms for sufficient accuracy (12-13 decimal digits) that they run too slowly." He notes that the best sin function he's come up with takes 500 ms to execute, compared to 340 ms for the BASIC sin function on the same machine. Would anyone care to recommend a good reference for this? We suppose that Falk would like a cookbook approach, rather that a course in numerical analysis.

Stocking Up

Dick Mesirov is the kind of reader we really appreciate: He sent us some original, unsolicited input — a "sponse," not a re-sponse. (Go thou and do likewise!) Here's how he tells it:

"I am a market maker on the Philadelphia Stock Exchange Options floor. If you've seen TV shots of those guys screaming, hollering, and waving their arms in the commodity pits, well, that's what I do. I store data on the stocks I trade: opening price, high, low, close, and the direction of last trade (up or down). This data is entered and used daily, and I store it by month. That is, all of October's entries are stored in the file named xxxOCT83, where xxx is the three-letter symbol for the stock.

"When I run the program each evening, after entering the day's data, I recall several months' data in addition to the current month's. The number of months required varies depending on what I am trying to do. Originally I recalled each month by name; then I came up with the routine listed here. It recalls and saves one month's data a day at a time, then automatically steps down by one month and recalls the prior month's data. After getting January's data it also steps down one year.

"I've never seen anything like it in any of the books I have nor in any of the commercial programs I've looked at. The same approach could be used by day or I guess for any series that can be listed as a string."

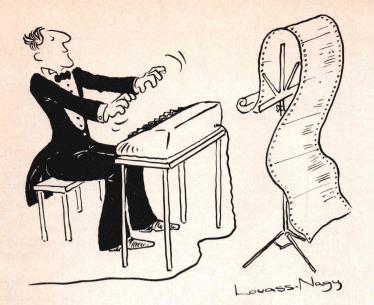
Dick's routine appears in Listing One (below). The original was cleanly structured using IFs and GOTOs; we took the liberty of recoding it to use nested FOR and WHILE loops to emphasize its shape. The only problem we can see is not in the program but in CP/M. There is a definite limit to the number of files you can store on a disk. Typically there are only 64 or 128 entries in the disk directory. When Mesirov accumulates a year's data on ten stocks he can look forward to getting a file error from filling up the directory. Within that limit, this looks like a useful technique.

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Dr. Dobb's Clinic (Text begins on page 9)

```
9000 REM
           SUBROUTINE TO READ STOCK HISTORY FILES
9010 REM
9020 REM history files are named SSSMMMYY, where
           SSS=stock-id, MMM=month, YY=year
9040 REM returns N=number of entries read
9050 REM
9060 IM$="JANFEBMARAPRMAYJUNJULAUGSEPOCTNOVDEC"
9070 INPUT "NAME OF STOCK"; ST$
9080 INPUT "CURRENT MONTH"; MO$
9090 B=INSTR(IM$,MO$)
9100 INPUT "CURRENT YEAR": YC$: YC=VAL(YC$)
9110 INPUT "HOW MANY MONTHS' DATA NEEDED"; NM
9120 N=0
9130 FOR JM=1 TO NM
        REM read one file's (month's) data
9140
9150
        OPEN "I", #1, ST$+MO$+YC$
        WHILE NOT EOF(1): N = N+1
9160
           REM here READ a stock entry into various
9170
9180
           REM ..arrays subscripted by N
9190
        WEND : CLOSE
9200
        REM get the prior month and year
9210
        B=B-3
        IF B<1 THEN B=B+36: YC=YC-1: YC$=RIGHT$(STR$(YC),2)
9220
        MO$=MID$(IM$,B,3)
9230
9240 NEXT JM
9250 RETURN
```



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CP/M EXCHANGE

by Robert Blum

Last month I ran a preliminary application note for CP/M Plus that reportedly would optimize the access time to any disk file - especially one that was small enough to fit into buffer memory and was accessed more than once within the same program. After installing the patches on my system, there appeared to be a significant increase in overall system performance, although at that time I had nothing more concrete to go on than an impression.

How Fast Is It?

To find out how much of a speed advantage (if any) was gained, I wrote a small benchmark program to be used in comparing the performance between an off-the-shelf distribution system and a modified one. The benchmark program I wrote had five distinct phases. The first four phases performed preliminary maintenance functions: make four files; write 16K of garbage data to each file; close the four files; and open the same four files that were just created.

The fifth and final phase sequentially read and reread the four files four times. If the changes were to be a success, this final phase of the program would produce the most dramatic evidence.

After completing the benchmark program, I copied the unaltered distribution files of my CP/M Plus system into a separate user area for use in link-editing the standard or unaltered CP/M system used in my benchmarks. My last step was to apply the patches outlined in the application note to one copy of my CP/M Plus system.

As shown in Table I (at right), four benchmarks were run, each with different combinations of features, etc. The first benchmark was run with two CP/M Plus systems that were completely stripped down and (as closely as I could make them) equal in performance to their V2.2 predecessor. As you can see, the runtime difference between the distribution and the modified system is small enough to be inconsequential.

The second and third benchmarks produced a healthy runtime reduction over the first one, but little difference was found between the two of them. This lack of improvement was a total surprise to me since I had expected the LRU buffering improvements to be more significant no matter what other options had been selected. But, as it appears, the LRU buffering logic is directly, or at least very closely, tied to the logic used for directory hashing.

Benchmark number four plainly points out how much of an improvement a plain vanilla CP/M Plus system can make in runtimes (not to mention the further reductions that can be experienced by tuning the system a little).

CP/M 2.2 BIOS Function: SELDSK

Billy Smith of Kentfield, California, writes:

"Here is a special treat for CP/M hackers. This tidbit just turned up as the root of a tricky little bug. I was implementing a public domain program called FILE.ASM at the time. Its function is to display all files on all drives and user areas matching the ambiguous file reference given in the command line. On my system it was stopping after completing drive A as if I had just done a cold boot and hadn't referenced any other drives yet.

"Debugging revealed that there is a slight difference in the version 2.2 CP/M BIOS function SELDSK from all earlier versions. There is an additional sentence in the V2.2 manual, under SELDSK, that explains a little further: 'The least significant bit of register E is zero if this is the first occurrence of the drive select since the last cold or warm start.' There was no reference to register E having to be preset to any special value in earlier versions of the CP/M manual.

"My BIOS (Morrow) takes advantage of this information and does not do a formal selection of a drive if the bit is nonzero. Instead it simply returns a value from a local variable that is assumed properly set during the first disk select. I suppose CP/M keeps this bit correct when calling SELDSK, but any program that does direct BIOS calls has the responsibility of managing this bit.

"One problem with this logic is that an application program has no idea if a drive has already been referenced or not and therefore must always set the bit to zero for its first reference even though this may be redundant to the BIOS. Since this public domain program had random nonzero data in register E, my BIOS was returning a bad value from SELDSK. In fact the bad value happened to be zero, which the program interpreted to mean non-existent drive and, of course, ended its operation.

"My simple fix was to clear register E before the call to SELDSK. Every call to SELDSK is now treated as though it were the first disk select of that drive and

a proper value is returned."

Application Note 2

Michael Carter of Garran, Australia, wrote several months ago to share a patch he had developed for reversing the meaning of the BACKSPACE and DELETE keys in CP/M V2.2. Michael's note prompted me to dig through my files to see if DR had officially released anything

Run	Distribution	Modified
#1	76 seconds	74 seconds
#2	64 seconds	59 seconds
#3	62 seconds	61 seconds
#4	37 seconds	25 seconds
Legend		

- #1 No features enabled, 1 Directory buffer and 1 Data buffer
- #2 Directory Hashing enabled, 1 Directory buffer and 1 Data buffer
- #3 No features enabled, 23 Directory buffers and 255 Data buffers
- #4 Directory Hashing enabled, 23 Directory buffers and 255 Data buffers

Table I.

Distribution vs. Modified CP/M Plus

on this subject. Sure enough, in February of 1982 an application note was published that thoroughly covers the subject.

CP/M® V2.2 Application Note 02, 2/20/82: Reversing the BACKSPACE and RUB-OUT Key Functions and Making RUBOUT Identical to BACKSPACE

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Applicable products and version numbers: CP/M® V2.1 and V2.2

Program: BDOS

In the following code segment procedures, addresses given are hexadecimal offsets from the base of the CP/M system. The CCP is usually located at 980H but can be located at A00H if a two-sector boot is used.

You can assemble the patch for your size memory system. The cpmbase equals the BDOS entry point address at locations 6 and 7 in the base page of memory minus 806H. You must change this entry point address when you load DDTTM or SIDTM. Under DDT or SID, follow the jump at location 5 until an address is found with a least significant digit of 6. In the following example, the cpmbase would be E506H-806H or DD00H.

0005	JMP	CD00
CD00	JMP	D3A4
D3A4	XTHL	
D3A5	SHLD	E452
D3A8	XTHL	
D3A9	JMP	E506

Procedure to reverse the BACKSPACE and RUBOUT key functions:

Patch into the SYSGEN or MOVCPM image exactly as you would patch in a new version of your BIOS, using the DDT i command followed by the DDT r command. You can use the same offset as your custom BIOS and install the code found in Listing One (page 14).

Patch into the SYSGEN or MOVCPM image exactly as you would patch in a new version of your BIOS, using the DDT i command followed by the DDT r command. Use the same offset as your custom BIOS and install the code in Listing Three (page 14).

Or, you can install the above procedure directly into MOVCPM if you have MOVCPM.COM on your system disk. The patch is installed automatically in any size system that you build using MOVCPM. Make a back-up copy of MOVCPM.COM before using DDT to make the following changes:

A>ddt movcpm.com DDT VERS 2.2 NEXT PC

2700 0	100	
-1141	b	
141B	MOV	A,B
141C	ORA	A
141D	JZ	09EF
1420	MOV	A,M
1421	DCR	В
-a141	b	
141B n	nvi a,8	

A> save 38 movcpml.com

141D jmp a07

1420 .

-g0

Use the new program MOVCPM1.COM in place of MOVCPM.COM. The

RUBOUT and BACKSPACE key functions are identical in any CP/M system generated with MOVCPM1.COM.

Licensed users are granted the right to include these modifications in CP/M V2.2 software.

Or, you can install the above procedure directly into MOVCPM if you have MOVCPM.COM on your system disk. The patch is applied automatically to any size system that you build using MOVCPM. Make a back-up copy of MOVCPM.COM before using DDT to make the following changes:

A>ddt movcpm.com DDT VERS 2.2 NEXT PC

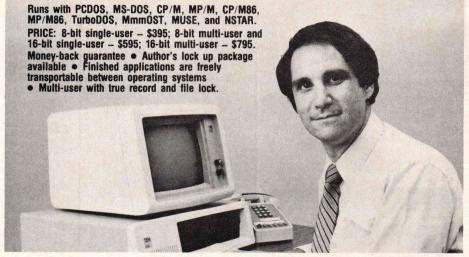
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140C	DCR	В
140D	LDA	0B0C
1410	STA	0B0A
1413	JMP	0A70
1416	CPI	7F
1418	JNZ	0A26
-s140	3	
1403 0	8 7f	
1404 (22 •	
-s141	7	
1417 7	f 8	
1418 (22 •	

-g0

A>save 38 movcpml.com

Use the new program MOVCPM1. COM in place of MOVCPM.COM. The BACKSPACE and RUBOUT key functions are reversed for any CP/M system generated with MOVCPM1.COM.

Procedure to make RUBOUT identical to BACKSPACE:

Before you install this patch, the code at cpmbase + 0A1Bh should read as shown in Listing Two (below).

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CP/M Exchange Listing (Text begins on page 12) Listing One

cpmbase	equ	?	;subtrack 806h	from	address
			at location 6		
	org	cpmbase	+ ØAØ2h		
	cpi	7fh	;was cpi Ø8h		
	org	cpmbase	+ ØA16h		
	cpi	Ø8h	;was cpi 7fh		

End Listing One

Listing Two

mov	a,b		
ora	a		
jz	cpmbase -	+	Ø9EFh
mov	a,m		
dcr	b		
dcx	h		
jmp	cpmbase -	+	ØAA9h

End Listing Two

Listing Three

cpmbase;	equ	?		
;	org	cpmbase	+	ØAlBh
	mvi jmp end	a,8h cpmbase	+6	ØAØ7h

End Listing

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RSA: A Public Key Cryptography System, Part I

ryptography has been in existence since the beginning of written history. Most of these ciphers and codes were developed for military applications. One of the more well-known systems, designed by Julius Caesar, is a simple letter transposition cipher that is easily broken today. Since that time, cryptography has matured; some current systems are estimated to be unbreakable, even using state-of-the-art technology. Some claim that cryptographers would have to spend millions of years using today's fastest computers to break some of these ciphers.

Key Systems

In the late 1970s, IBM introduced a single key system that was later made into a standard by the National Bureau of Standards. The Data Encryption Standard (DES) is a 56-bit key system that has been committed to silicon by several semiconductor vendors. The cipher is fast and easy to integrate into LSI circuits. During and since its standardization, controversy has raged over the security of the DES. The primary concern is over the short length of the key. Many also speculate that since the government, which has overriding national security concerns, standardized the cipher, they may have a means of breaking it. However, many groups use the DES, including financial institutions, local network manufacturers, and others.

The Public Key System (PKS) was first proposed by W. Diffie and M. E. Hellman in 1976. Their paper described a dual key system with keys generated from large prime numbers (100+ decimal digits). In 1979, Hellman published an article in Scientific American describing the mathematics of the PKS. He also offered a monograph that described the system in greater detail, but within a year of the monograph offering, it was withdrawn from public distribution. Specula-

by C. E. Burton

C. E. Burton, 1720 S. Deframe Court, Denver, CO 80228.

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"In this two-part series of articles, we will discuss the Rivest-Shamir-Adleman (RSA) PKS and show how to implement it on a microcomputer."

tion was that the government (NSA and DoD) had forced its withdrawal because it jeopardized the national security. Unlike the DES cipher, the PKS has not been committed to silicon because it is much more complex and requires considerably more mathematics and computation time, as we will soon see. Currently, investigators continue ongoing research efforts to generate efficient integrated circuit implementations of the PKS.

In 1978, R. L. Rivest, A. Shamir, and L. Adleman showed how the PKS could be implemented and proposed a means of providing digital signatures to the messages.³ The addition of a signature to a message allows a sender to sign his message so that a receiver can be sure that the message originated with the sender. The February 1983 issue of IEEE Computer had a series of articles discussing ways to compromise digital signatures and how they could be made more secure.^{4, 5, 6, 7} Some of the articles examined several different ciphers, their basic implementations, and their limitations.

In this two-part series of articles, we will discuss the Rivest-Shamir-Adleman (RSA) PKS and show how to implement it on a microcomputer. The first part will discuss RATFOR (the implementation language) and the mathematical core of the RSA system, including modulo arithmetic, multiple-precision arithmetic, and "Russian Peasant" exponentiation. In the second part, we will describe the generation of the keys (public and private) and the encryption/decryption system; we will also take a brief look at digital signatures.

RATFOR

RATFOR (RATional FORtran) is a preprocessor to Fortran; i.e., the output of the RATFOR precompiler is Fortran. The language is described in the book Software Tools. RATFOR adds several structured constructs to Fortran while allowing use of standard Fortran statements, in-line comments, free-form input, multiple statements in a line, definitions, inclusion of other files, logical conditions

similar to BASIC or C, a standard library, and so on. In a lot of ways, it is similar to the C programming language, and those familiar with C should have little trouble transporting RATFOR source to C source. For that matter, those familiar with Pascal should be able to transport the software easily.

Those who want to use RATFOR can obtain a public domain version from the CP/M Users Group (CPMUG), operated by LifeBoat Associates, or from one of the RCPM bulletin boards that has it available for downloading. Unfortunately, a library is unavailable with the public domain version; however, using the Software Tools text, you can generate your own library of functions or simply pull out the appropriate functions from the precompiler.

Now let's review some of the rudiments of the language. The structured constructs that RATFOR adds to Fortran include:

```
repeat

f
:
statement n
:
}
until (condition)
while (condition)

f
:
statement n
:
}
for (init. statement; condition; loop
statement)

f
:
statement n
:
}
do n=j,k # Note the lack of a ter-
# minating label number

f
:
statement n
```

RATFOR uses # as an in-line comment indicator and ignores anything after its occurrence to the end of the line. The { and } symbols are used as Begin and End designators, respectively. Within the iterative constructs, you can use the special words break and next; break causes an exit from the innermost loop, while next causes the innermost loop to continue execution at its "condition" statement. Because of the form of the "if" statement that RATFOR uses, you cannot use a Fortran arithmetic IF within RATFOR. For example,

if (condition) less-label, equal-label, greater-label

is a "no-no."

The "condition" statements can contain any of the following logical operators:

== Equals

!= Not Equal

< Less Than

<= Less Than or Equal

> Greater Than

>= Greater Than or Equal

! Not

& And

Or

The last three logical operators, !, &, and !, can also be used as logical bitwise operators on logical or integer variables.

Two other special words that are available are define and include. define allows you to make substitutions of strings within the source file. Beginning where it occurs within the source, it substitutes the right string for the left string throughout the text. An example follows:

define(Yes,1) # substitute "1" for #"YES" before processing

include allows you to include another file at the point that the statement occurs.

For example:

include RATDEF # reads and com-# piles the file RATDEF.RAT

Multiple statements per line are handled using semicolons, as shown below:

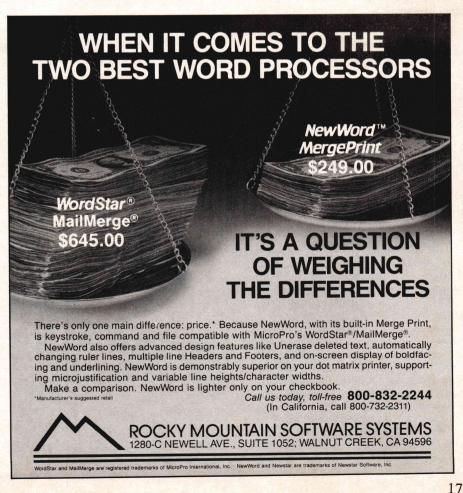
statement 1; statement 2; statement 3

Statements can also be extended across multiple lines. The "condition" statements are automatically extended; statements with comma separators are extended if the comma ends the line; but other statements must use an underscore (_) to extend across a line boundary. Figure 1 (above) illustrates these three extensions. The precompiler limits the line to 72 characters, generates a continuation character in column 6 of the next physical line, and continues the source in this

fashion until the logical line ends. To get an idea of how RATFOR handles these items, consult Listing One (page 22) and Listing Two (page 24) for examples of the RATFOR source and Listing Three (page 24) for the Fortran source generated by the precompiler.

Modulo Arithmetic

At the heart of a PKS cipher is the ability to perform multiple-precision, nonnegative, integer arithmetic. As was implied earlier, the PKS cipher requires the use of addition, subtraction, multiplication, division, and exponentiation of large numbers (100 to 250 decimal digits). Most computers, including microcomputers, are limited to arithmetic on numbers



having 3 to 10 decimal digits (usually in binary form). To compound the problem, performing the RSA algorithms requires the use of modulo arithmetic. In some respects, modulo arithmetic is a lifesaver in that it limits the size of the numbers that are generated; however, it also complicates things because additional processing must be done on the results of arithmetic operations.

While modulo arithmetic is similar to the arithmetic that we learned in school, it has some differences. Thus, we should look at a few of its properties. A "modulus" is essentially the base of the number system that is being employed, and the mod operator leaves the "residue" of numbers. We could define the operation (A mod B) for nonnegative integers (in which we are currently interested) as:

Therefore, a number system with a base of 10 (decimal) can only contain the digits 0, 1, 2, 3, 4, 5, 6, 7, 8, and 9. The following example uses hex (base 16):

Another interesting property of modulo arithmetic is:

$$(A @ B) \bmod C = ((A \bmod C) @ (B \bmod C)) \bmod C$$

where @ can represent +, -, or *. Exponentiation is similar:

$$(A ** B) \mod C =$$

 $((A \mod C) ** B) \mod C$

These operations are also commutative; i.e., the order of performance is not important, as illustrated in Figure 2a (at right). Figure 2b (below) shows a couple of examples to make sure we have it.

Now you can understand why using modulo arithmetic in raising large numbers to large powers can be a lifesaver. However, it does add considerably more operations (one division to get the remainder after each operation).

Multiple-Precision Arithmetic

In this section, we will cover the basic operations of addition, subtraction, multiplication, and division of nonnegative integers. I should point out that there are a few programs capable of performing multiple-precision arithmetic on very large numbers, e.g., MUMATH. These programs undoubtedly use similar algorithms to perform their operations, and I am sure that they could be put to use effectively in implementing the RSA algorithm. Our current objective, however, is to show how to do it.

The general opinion is that D.E. Knuth has written the "bible" of computer algorithms, and his work is the basic source of most of what I will subsequently pre-

sent in this set of articles. Many of the extensions to what he presents in the text of his books can be found in the problems at the end of each section and in the subsequent answers at the end of each book. Another paper dealing with multiple-precision arithmetic that might be helpful here was written by M. Zimmerman. My implementation of the multiple-precision arithmetic (MPARITH.RAT) can be found in Listing Four (page 26).

Looking at the basic arithmetic operations, we can say that:

- Addition of two N-digit numbers produces a result that has at most (N+1) digits.
- Subtraction of two N-digit numbers produces a result that has at most N digits.
- Multiplication of an N-digit number by an M-digit number produces a result that has at most (N+M) digits.
- Division of an (N+M)-digit number by an N-digit number produces a quotient of at most (M+1) digits and a remainder of at most N digits.

These statements hold true no matter what radix (number base) system is used. In my particular case, I have used a radix of 128, where each byte holds a number between 0 and 127, because the Fortran that I used (Microsoft F80) does two's-complement arithmetic on byte variables. A define statement near the beginning of the routines to be presented in Part II sets BYTEMODULUS to 128. The MPARITH routines shown in Listing Four, however, are not limited to this value. They allow any byte modulus (radix) between 2 and 128, and the value is passed to the subroutine by the parameter MODULO.

Knuth, in his set of books dealing with computer programming, presents both algorithms and the "MIX" assembly language programs that implement the algorithms. I have implemented a RATFOR (Fortran) version of the algorithms. No doubt, an assembly version of the algorithms written in Microsoft's M80 would run considerably faster, but I was more interested in transportability and ease of understanding than in speed. (A basic premise of programming is to get it working first then to modify things to optimize performance only if it is necessary. Another premise is that if it ain't broke, don't fix it!)

I will present Knuth's algorithms here without going into detail on my routines.* Since RATFOR allows in-line comments, the code should include most of the re-

*Donald E. Knuth, The Art of Computer Programming, Vol. 2, © 1981, Addison-Wesley, Reading, Massachusetts. Pgs. 250, 252, 253, 254, 257, 258 and 442 (to include Algorithms "A", "S", "M", "D", "A"). Reprinted with permission.

```
(A @ B) mod C = (B @ A) mod C

(A ** (B*C)) mod D = ((A ** B) ** C) mod D

= (((A ** B) mod D) ** C) mod D

- or -

= ((A ** C) ** B) mod D

= (((A ** C) mod D) ** B) mod D

Figure 2a.
```

```
(5 * (6+2) - 4) mod 7 = 36 mod 7 = 1
	= (5 * (8 mod 7) - 4) mod 7 = (5 - 4) mod 7 = 1
	(3 ** (4*2)) mod 7 = 6561 mod 7 = 2
	= (((3 ** 4) mod 7) ** 2) mod 7
	= ((81 mod 7) ** 2) mod 7 = (4 ** 2) mod 7
	- or -
	= 16 mod 7 = 2
	= (((3 ** 2) mod 7) ** 4) mod 7
	= ((9 mod 7) ** 4) mod 7 = (2 ** 4) mod 7
	= 16 mod 7 = 2
	Figure 2b.
```

quired documentation, especially when it is compared to the original algorithms. A transcription of these algorithms follows with some minor rewording. These algorithms are presented for those of you who do not have access to Knuth's second volume.

Addition Algorithm

ALGORITHM A (addition of nonnegative integers). Given nonnegative Nplace integers (u1, u2,..., un) radix b and (v1, v2,..., vn) radix b, this algorithm forms their sum (w0, w1, w2,..., wn) radix b. (Here w0 is the carry, and it will always be 0 or 1.)

- A1. [Initialize] Set j=n, k=0. (The variable j will run through the various digit positions, and the variable k keeps track of carries at each step.)
- A2. [Add digits] Set wj = (uj + vj + k) mod b and k = floor((uj + vj + k) / b). (In other words, k is set to 1 or 0, depending on whether a carry occurs or not, i.e., whether uj + vj + k > = b or not. At most one carry is possible during the two additions, since we always have uj + vj + k < = 2 * (b 1) + 1 < 2 * b, by induction on the computation.)
- A3. [Loop on j] j=j-1. If j > 0, then go to A2; else set w0=k and terminate.

Note: floor(x) is the greatest integer less than or equal to x, also known as int(x).

Subtraction Algorithm

ALGORITHM S (subtraction of nonnegative integers). Given nonnegative Nplace integers (u1, u2,..., un) radix b >= (v1, v2,..., vn) radix b, this algorithm forms their nonnegative difference (w1, w2,..., wn) radix b.

- S1. [Initialize] Set j=n, k=0.
- S2. [Subtract digits] Set wj = (uj vj + k) mod b and k = floor((uj vj + k) / b). (In other words, k is set to -1 or 0, depending on whether a borrow occurs or not, i.e., whether uj vj + k < 0 or not. In the calculation of wj, note that we must have $-b \le uj vj + k \le b$ or $0 \le uj vj + k + b \le 2$
- S3. [Loop on j] j=j-1. If j>0, then go to S2; else terminate. (When the algorithm terminates, k should be 0; the condition k=-1 will occur if and only if V>U, and this is contrary to the assumptions.)

Multiplication Algorithm

ALGORITHM M (multiplication of nonnegative integers). Given nonnegative integers (u1, u2, ..., un) radix b and (v1, v2, ..., vm) radix b, this algorithm forms the product (w1, w2, ..., wl) radix b, l=m+n. (The conventional pencil-and-paper method is based on forming the partial products (u1, u2, ..., un) * vj first, for $1 \le j \le m$, and then adding

these products together with appropriate scale factors; in a computer it is best to do the addition concurrently with the multiplication, per this algorithm.)

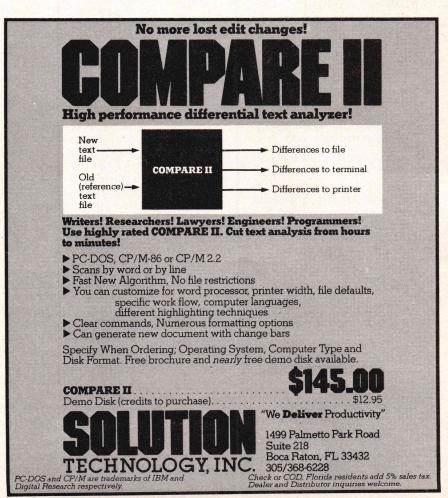
- M1. [Initialize] Set wj=0, $m+1 \le j \le m+n$. Set j=m. (If wj above were not set to zero in this step, it turns out that the steps below would set $W=U*V+(wi,\ldots,wj)$, i=m+1 and j=m+n. This more general operation is sometimes useful.)
- M2. [Zero multiplier?] If vj = 0, set wj = 0 and go to Step M6. (This test saves a good deal of time if there is a reasonable chance that vj is zero, but otherwise it may be omitted without affecting the validity of the algorithm.)
- M3. [Initialize i] Set i=n and k=0.
- M4. [Multiply and add] Set t = ui * vj + (wr + k), r = i + j; then set wr = (t mod b) and k = floor(t/b). (Here the carry k will always be in the range $0 \le k \le b$.)
- M5. [Loop on i] i=i-i. Now if i > 0, go back to Step M4; otherwise set wj=k.
- M6. [Loop on j] j=j-1. Now if j > 0, go back to Step M2; otherwise the algorithm terminates.

Division Algorithm

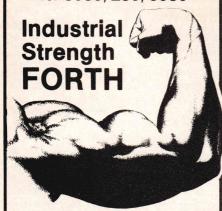
ALGORITHM D (division of nonneative integers). Given nonnegative integers (u1, u2,..., ul) radix b, l=m+n, and (v1, v2,..., vn) radix b, where v1 != 0 and n > 1, we form the quotient floor (U/V) = (q1, q2,..., qm) radix b and the remainder $U \mod V = (r1, r2,..., rn)$ radix b.

- D1. [Normalize] Set d = floor(b/(v1+1)).

 Then set $(u0, u1, \ldots, ul)$ radix b, l=m+n, equal to $(v1, v2, \ldots, vn)$ radix b times d. (Note the introduction of a new digit position u0 at the left of u1; if d=1, all we need to do in this step is to set u0=0. On a binary computer it may be preferable to chose d to be a power of two instead of using the value suggested here; any value of d that results in v1 >= floor(b/2) will suffice.)
- D2. [Initialize j] Set j=0. (The loop on j, Steps D2 through D7, will be essentially a division of (uj, \ldots, ur) radix $b, r=j+n, by (v1, \ldots, vn)$ radix b to get a single quotient digit qj.)
- D3. [Calculate q'] If uj=v1, set q'=b-1; otherwise set q' = floor((uj*b+ur)/v1), r=j+1. Now test if v2*q' > (uj*b+ur-q'*v1)*b+us, r=j+1 and s=j+2; if so, decrease q' by 1 and repeat this test. (The latter test determines at high speed most of the cases in which the trial value q' is one too large, and it eliminates all cases where



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- D4. [Multiply and subtract] Replace (uj, ..., ur) radix b, r=j+n, by (uj, ..., ur) radix b minus q' times (vl, ..., vn) radix b. This step (analogous to Steps M3, M4, and M5 above) consists of a simple multiplication by a one-place number, combined with a subtraction. The digits (uj, ..., ur) radix b should be kept positive; if the result of this step is actually negative, (uj, ..., ur) radix b should be left as the true value plus b ** (n+1) (i.e., as the b's complement of the true value) and a borrow to the left should be remembered.
- D5. [Test remainder] Set qj =q'. If the result of Step D4 was negative, go to Step D6; otherwise go to Step D7.
- D6. [Add back] (The probability that this step will be necessary is very small, on the order of only 2/b; test data that activate this step should therefore be specifically contrived when debugging.) qj=qj-1, and add (0, v1,..., vn) radix b to (uj,..., ur) radix b, r=j+n. (A carry will occur to the left of uj, and it should be ignored since it cancels with the borrow that occurred in D4.)
- D7. [Loop on j] j=j+1. Now if $j \le m$, go back to D3.
- D8. [Un-normalize] Now (q0, q1,..., qm) radix b is the desired quotient, and the desired remainder may be obtained by dividing (ur,..., us) radix b, r=m+1 and s=m+n, by d.

As you can see, the implementation of these algorithms in Listing Four follows very closely, with the exception of some differences or additions that were required to make things work properly. A few areas are also identified in the listing where additions to the algorithms can be implemented. Obviously, these algorithms are very computation-intensive, and they form the root of the rest of the software. If we were to expend effort to optimize these routines, a real savings in execution time could undoubtedly be realized. A possibility would be to change the number arrays from byte-types to integertypes and to increase the arithmetic modulus MODULO. However, this change would create additional complications in handling text files because character packing would now have to be done. Another method to speed up the operations would be to use an arithmetic coprocessor, e.g., an AMD 9511 or an Intel 8087. Speed increases of up to an order of magnitude could potentially be realized.

We are now ready to discuss the final mathematical algorithm in this part of the article, the "Russian Peasant" algorithm. It will use the multiple-precision arithmetic algorithms just presented.

Russian Peasant Exponentiation

This algorithm was given its name by nineteenth-century visitors to Russia who found the technique in wide use there. However, the algorithm appeared in the fifteenth century and is based on a multiplication technique developed by the Egyptians at least 3300 years earlier. In certain aspects, the algorithm is similar to a binary search. The algorithm, as stated by Knuth, follows.

Russian Peasant Algorithm

ALGORITHM A (right-to-left binary method for exponentiation). This algorithm evaluates X **N, where N is a positive integer.

- A1. [Initialize] Set n=N, y=1, z=X.
- A2. [Halve n] (At this point $X^*N = y^*$ (z^*n).) Determine whether n is odd or even. Set n=floor(n/2). If n is even, go to Step A5.
- A3. [Multiply y by z] Set y = y * z.
- A4. [Check n for 0] If n = 0, the algorithm terminates with y as the answer.
- A5. [Square z] Set z = z * z, and go to Step A2.

It should be pointed out that a dual algorithm for multiplication (y = X * N)can be achieved by replacing y=1 with y=0 in Step A1; by replacing Step A3 with an addition, i.e., y = y + z; and by replacing Step A5 with a doubling instead of a squaring, i.e., z = z + z. J. Nyberg has presented a BASIC program to perform this algorithm. 11 This method of exponentiation is usually suitable only for large exponents, N, since multiple-precision multiplication is more efficient for small and moderate values of N. Some faster methods are available. One involves the factoring of N into its prime factors. Another method uses a technique called "power trees." These methods are explained in more detail in Knuth's book, but these techniques are more complex than the Russian Peasant method.

The Russian Peasant algorithm had to be broadened to perform Y = (X ** N) mod M. The modulo operation limits the resultant answer to $0 \le Y \le M$. We shall see in the next part of the article that this form of the algorithm is required. The modification to the algorithm is as follows:

- A5. [Square z] Set z = z * z.
- A6. [Limit z] Set $z = (z \mod M)$ and go to A2.

The implementation of the expanded algorithm (RPEASANT.RAT) is found in Listing Five (page 38). The listing contains three subroutines:

RPEXP – The modified Russian Peasant algorithm.

PRDMOD – The routine that performs (Z * Z) mod M. It uses the multiple-

precision multiplication and division routines given in Listing Four.

MAKBIN - This routine tries to make the halving of the exponent, N, somewhat more efficient. The byte modulus, MODULO, does not have to be 2 ** K, $0 < K \le 7$; thus, N cannot be strictly scanned on a bit-by-bit basis to perform the even/odd check and halving. So we must find a base modulus so that BASMOD = 2 ** ceiling (log2(MODULO)), and use it to strip off bytes that can be scanned on a bit-by-bit basis in the even/odd check. The operation becomes:

- 1. Initially, set Nx = N (first pass through)
- 2. TEMP = remainder(Nx/BASMOD)
- 3. Nx = floor(Nx / BASMOD) = quotient(Nx / BASMOD)

where TEMP is used by RPEXP to do Step A2 and the new Nx is retained for the next pass until Nx = 0. If MODULO = 2 ** K, TEMP just returns the current least-significant byte of Nx without doing the division. Therefore, it is most efficient to make $MODULO = member\{2,4,8,16,32,64,$ 1287.

As you can see in the multiple-precision routines and in the Russian Peasant routine, a considerable amount of pointer manipulation occurs. These routines probably could be implemented more efficiently in C or in Pascal, where pointer manipulation is more easily attained. I do not claim that these implementations are optimal; however, they do work. If you feel the need to "optimize" the code, dig in!

Summary

We are now at the end of the first part of the article. We have covered several areas: RATFOR, modulo arithmetic, multiple-precision arithmetic, and Russian Peasant exponentiation. If you implement these algorithms on your system, you should be able to check the addition and subtraction algorithms fairly easily. To check out the multiplication, division, and exponentiation algorithms, you will need a calculator with a large digit accuracy (>= 10 is desirable) or a copy of MUMATH or TK!SOLVER. Try this problem:

> $D = (A ** B) \mod C$ using A = 9182736450B = 1928374605C = 12345678907Byte Modulo = 100 to obtain D = 10447988731

In the second and final part, we will look at the RSA-PKS encryption/decryption implementation. This will cover three additional areas: the generation and testing of large prime numbers, the generation of the public/private keys, and the encryption/decryption of text files with a brief look at digital signatures. See you on the second pass.

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RSA Cryptography System (Text begins on page 16) Listing One

```
# *** Listing 1 -- an example of the RATFOR precompiler output
 ***
#
               Note that the resulting FORTRAN has the spaces and new lines
               deleted and that the result is much less readable than the
#
               RATFOR source !!!
define(NEWVARIABLE, variable30) # look at Label 8 for the replacement
program example
     integer variable1,variable2,variable3,variable4,variable5,variable6,
             variable7, variable8, variable9, variable10
1
     continue # separator for use in finding where statements begin in
                 generated FDRTRAN code (standard FDRTRAN statement)
     repeat # a RATFOR Construct
          { # "{" and "}" are not needed for a single statement (see WHILE) !!!
          statement a
     until (variable1 == variable2)
     continue
     while (variable3 != variable4)
          statement b # in-line comment
3
     continue
     for (variable5=0.0; variable5 < variable6; variable5=variable5+1.0)
          statement c
          if (variable7 >= variable8)
               break
          3.
4
     continue
     do n=j,k
               # Note the lack of a terminating label number.
                 will fill it in for you !!!
          if (variable9 >= variable10)
               next
          statement d
5
     continue
     if (variable7 > variable8)
```

(Continued on page 24)

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RSA Cryptography System (Listing continued, text begins on page 16) **Listing One**

```
{ # "{" and "}" must be used here since ";" makes multiple lines !!!
     statement e; statement f; statement g
else if ((! logical1) & (variable9 <= variable10) {
         (logical2 & logical3))
     statement h
else
     statement i
```

include LISTING2 # insert Listing 2 here

End Listing One

Listing Two

```
# *** Listing 2 -- this file will be included into Listing 1 at the occurrance
# ***
                   of the "include LISTING2" statement !!!
     call subroutine(variable11, variable12, variable13, variable14, variable15,
                     variabl16, variable17, variable18, variable19, variable20)
     variable21=variable22*variable23/variable24+variable25-
                (variable26/variable27)+variable28
     variable31=NEWVARIABLE
```

End Listing Two

Listing Three

```
C *** Listing 3 -- resultant FORTRAN source generated by passing
C ***
                    Listing1 & Listing2 through RATFOR
C
 ***
      programexample
      integervariable1, variable2, variable3, variable4, variable5, variable6
     *, variable7, variable8, variable9, variable10
      continue
      continue
23000 continue
      statementa
23001 if(.not.(variable1.eq.variable2))goto 23000
23002 continue
      continue
      continue
23003 if(.not.(variable3.ne.variable4))goto 23004
      statementb
      goto 23003
23004 continue
3
      continue
      continue
      variable5=0.0
```

(Continued on page 26)

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RSA Cryptography System (Listing continued, text begins on page 16) Listing Three

```
23005 if(.not.(variable5.lt.variable6))goto 23007
      statementc
      if(.not.(variable7.ge.variable8))goto 23008
      goto 23007
23008 continue
23006 variable5=variable5+1.0
      goto 23005
23007 continue
      continue
      do 23010n=j.k
      if(.not.(variable9.qe.variable10))goto 23012
23012 continue
      statementd
23010 continue
23011 continue
      continue
      if(.not.(variable7.gt.variable8))goto 23014
      statementf
      statementq
      qoto 23015
23014 continue
      if(.not.((.not.logical1).and.(variable9.le.variable10).or.(logical
     *2.and.logical3)))goto 23016
      statementh
      goto 23017
23016 continue
      statementi
23017 continue
23015 continue
      callsubroutine(variable11, variable12, variable13, variable14, variabl
     *e15, variabl16, variable17, variable18, variable19, variable20)
7
      variable21=variable22*variable23/variable24+variable25-(variable26
     */variable27)+variable28
8
      variable31=variable30
      end
```

End Listing Three

Listing Four

```
###
###
                                                          ###
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                                                          ###
###
                                                          ###
### All rights reserved. Permission granted to use this software for
                                                          ###
###
    personal, non-commercial purposes only.
                                                          ###
###
# PROGRAM NAME:
             MPARITH.RAT
# PURPOSE:
         Unsigned Multiple Precision Arithmetic Routines (re: D.E. Knuth,
         The Art of Computer Programming, V. 2 (Semi-Numerical Algorithms),
#
         2nd Ed., (Addison-Wesley, Reading, MA), pp. 250-268.)
#
                MPADD -- Multiple Precision Addition
#
                MPSUBT -- Multiple Precision Subtraction
                MPMULT -- Multiple Precision Multiplication
                     -- Multiple Precision Division
```

LANGUAGE: RATFOR # AUTHOR: CEB

#

#

#

USAGE: CALL MPADD(NUM1,LEN1,NUM2,LEN2,SUM,LENS,MODULO)
CALL MPSUBT(NUM1,LEN1,NUM2,LEN2,DIFF,LEND,MODULO)
CALL MPMULT(NUM1,LEN1,NUM2,LEN2,PROD,LENP,MODULO)

CALL MPDIV (NUM1, LEN1, NUM2, LEN2, QUOT, LENQ, REMN, LENR, MODULO)

<>NUM* -- Byte array, contains the number (byte modulus MODULO)
 to be operated on. The MSDigit(s) are in
 NUM*(1) and the LSDigit(s) are in NUM*(LEN*). NUM2 is

never modified and NUM1 is only modified by MPDIV.
CAUTIONS: for MPSUBT, NUM1 must be the larger number;

for MPDIV, NUM1 must be the Numerator such that NUM1(1) == 0 (required for normalization) and NUM2 must be the Demominator such that NUM2(1) != 0.

>LEN1/2 -- Integer variable, defines the length of the NUM* array.

(SUM -- Byte array, contains the sum (byte modulus MODULO) of NUM1 + NUM2. The MSDigit(s) are in SUM(1) and the LSDigit(s) are in SUM(LENS).

>LENS -- Integer variable, defines length of SUM array.

CAUTION: LENS = MAX(LEN1, LEN2) + 1

(DIFF -- Byte array, contains the difference (byte modulus MODULO) of NUM1 - NUM2. The MSDigit(s) are in DIFF(1) and the LSDigit(s) are in DIFF(LEND).

>LEND -- Integer variable, defines length of DIFF array.

(Continued on next page)



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RSA Cryptography System (Listing continued, text begins on page 16) **Listing Four**

```
CAUTION: LEND = LEN1
               <PROD -- Byte array, contains the product (byte modulus MODULO)</pre>
#
#
                        of NUM1 * NUM2. The MSDigit(s) are in
                        PROD(1) and the LSDigit(s) are in PROD(LENP).
               >LENP -- Integer variable, defines length of PROD array.
                        CAUTION: LENP = LEN1 + LEN2
               <QUOT -- Byte array, contains the quotient (byte modulus MODULO)
#
#
                        of NUM1 / NUM2. The MSDigit(s) are in
#
                        QUOT(1) and the LSDigit(s) are in QUOT(LENQ).
#
               >LENQ -- Integer variable, defines length of QUOT array.
                        CAUTION: LENQ = MAX(LEN1 - LEN2, 1)
#
               <REMN -- Byte array, contains the remainder (byte modulus)</p>
                         MODULO) of NUM1 / NUM2. The MSDigit(s) are in
#
                         REMN(1) and the LSDigit(s) are in REMN(LENR).
#
                >LENR -- Integer variable, defines length of REMN array.
#
                         CAUTION: LENR = LEN2
#
                >MODULO -- Integer variable, defines the arithmetic modulus that
#
#
                           is to be used. MODULO has a byte-wide effect and
                           should be between 2 and 128 (e.g. 100 for Decimal
#
                           Numbers and 128 for ASCII Characters).
#
                           CAUTION: The Arrays MUST have the same modulus and
#
                           MUST be positive (unsigned) for proper operation !!!
#
#
# ARRAYS USED: NUM1(*), NUM2(*), SUM(*), DIFF(*), PROD(*), QUOT(*), REMN(*)
# EXTERNALS:
# UPDATE HISTORY: INITIAL RELEASE -- 01/18/83 CEB
subroutine mpadd(num1,len1,num2,len2,sum,lens,modulo)
     byte num1(1), num2(1), sum(1)
     integer len1, len2, lens, modulo, add, carry
     if (lens >= maxO(len1,len2)+1) # SUM array have sufficient length?
           idxsum=lens # get the index to the LSDigit of SUM
           idxn1=len1 # get index to LSDigit of NUM1
           idxn2=len2 # get index to LSDigit of NUM2
           carry=0 # initialize CARRY
           while (minO(idxn1,idxn2) > 0) # look at each packed character
                itemp1=num1(idxn1); itemp2=num2(idxn2) # byte to integer
                add=carry+itemp1+itemp2 # get sum
                sum(idxsum)=mod(add,modulo) # generate SUM value
                carry=add/modulo # generate CARRY for next pass
                idxn1=idxn1-1 # move indices to next position
                idxn2=idxn2-1
                idxsum=idxsum-1
           if (idxn2 == 0) # 1st number still has data?
                while (idxn1 > 0) # look at rest of 1st number
                     itemp1=num1(idxn1) # byte to integer
                     add=carry+itemp1 # get sum
                     sum(idxsum)=mod(add,modulo) # generate SUM value
                                                                   (Continued on page 30)
```

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RSA Cryptography System (Listing continued, text begins on page 16) Listing Four

```
carry=add/modulo # generate CARRY for next pass
                 idxn1=idxn1-1 # move indices to next position
                 idxsum=idxsum-1
         else # 2nd number still has data?
              while (idxn2 > 0) # look at rest of 2nd number
                   itemp1=num2(idxn2) # byte to integer
                   add=carry+itemp1 # get sum
                   sum(idxsum)=mod(add,modulo) # generate SUM value
                   carry=add/modulo # generate CARRY for next pass
                   idxn2=idxn2-1 # move indices to next position
                   idxsum=idxsum-1
         sum(idxsum)=carry # put final CARRY in SUM
         idxsum=idxsum-1 # move index next position
         while (idxsum > 0) # finish filling in SUM
              sum(idxsum)=0 # zero out remainder of SUM
              idxsum=idxsum-1 # move index to next position
    else # cannot generate SUM
         call rmrkln(".")
         call error('Length of [SUM] too small !!!.') # print message & exit
    return
end
subroutine mpsubt(num1,len1,num2,len2,diff,lend,modulo)
    byte num1(1), num2(1), diff(1)
    integer len1, len2, lend, modulo, subt, borrow
    if ((lend >= len1) & (len1 >= len2)) # DIFF array have sufficient length
                                          and NUM1 at least as big as NUM2?
         idxdif=lend # get the index to the LSDigit of DIFF
         idxn1=len1 # get index to LSDigit of NUM1
         idxn2=len2 # get index to LSDigit of NUM2
         borrow=0 # initialize BORROW
         while (idxn2 > 0) # look at each packed character
              itemp1=num1(idxn1); itemp2=num2(idxn2) # byte to integer
              subt=borrow+itemp1-itemp2 # get difference
              if (subt < 0) # need to do MODULO's complement?
                   subt=modulo+subt # do MODULO's complement
                   borrow=-1 # indicate borrow
              else # everything is okay
```

```
borrow=0
     diff(idxdif)=subt # generate DIFF value
     idxn1=idxn1-1 # move indices to next position
     idxn2=idxn2-1
     idxdif=idxdif-1
while (idxn1 > 0) # look at rest of 1st number
     itemp1=num1(idxn1) # byte to integer
     subt=borrow+itemp1 # get difference
     if (subt < 0) # need to do MODULO's complement?
          subt=modulo+subt # do MODULO's complement
          borrow=-1 # indicate borrow
     else # everything is okay
          borrow=0
     diff(idxdif)=subt # generate DIFF value
     idxn1=idxn1-1 # move indices to next position
     idxdif=idxdif-1
if (borrow == 0) # NUM1 >= NUM2?
     while (idxdif > 0) # finish filling in DIFF
          diff(idxdif)=0 # zero out remainder of DIFF
```

(Continued on next page)

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RSA Cryptography System (Listing continued, text begins on page 16) Listing Four

```
idxdif=idxdif-1 # move index to next position
         else # NUM1 < NUM2 (error)
              call rmrkin(".")
              call error('[NUM1] < [NUM2] in subtraction.') # print message
                                                             & exit
    else # cannot generate DIFF
         call rmrkln(".")
         call error('Length of [DIFF] too small or [NUM1] < [NUM2] !!!.')
                                                     # print message & exit
    return
end
subroutine mpmult(num1,len1,num2,len2,prod,lenp,modulo)
    byte num1(1), num2(1), prod(1)
     integer len1, len2, lenp, modulo, mult, carry
     if (lenp >= len1+len2) # PROD array have sufficient length?
         idxn2=len2 # get index to LSDigit of NUM2
         do idxprd=1,lenp # clean out PROD
              prod(idxprd)=0
         while (idxn2 > 0) # multiply numbers
              idxprd=lenp+(idxn2-len2) # get index to PROD
              if (num2(idxn2) == 0) # multiplier zero?
                   carry=0 # set CARRY (product) to zero
                   idxprd=idxprd-len1 # get index to PROD
              else # finite multiplier value
                   idxn1=len1 # get index to LSDigit of NUM1
                   carry=0 # initialize CARRY
                   while (idxn1 > 0) # multiply a "digit" at a time
                        itemp1=num1(idxn1); itemp2=num2(idxn2) # byte to
                        itemp3=prod(idxprd)
                                                              # integer
                        mult=carry+itemp1*itemp2+itemp3 # get product
                        prod(idxprd)=mod(mult,modulo) # generate new product
                                                        value
                        carry=mult/modulo # generate CARRY for next pass
                        idxn1=idxn1-1 # move indices to next position
                        idxprd=idxprd-1
                   3.
              prod(idxprd)=carry # save CARRY for next pass
```

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RSA Cryptography System (Listing continued, text begins on page 16) Listing Four

```
idxn2=idxn2-1 # move index to NUM2 to next position
    else # cannot generate PROD
          call rmrkin('.')
          call error('Length of [PROD] too small !!!.') # print message & exit
     return
end
**********************************
subroutine mpdiv(num1,len1,num2,len2,quot,leng,remn,lenr,modulo)
     byte num1(1), num2(1), quot(1), remn(1)
     integer leni,len2,leng,lenr,modulo,div,mult,add,borrow,carry,flag,scale,
             qtest
     equivalence (borrow, carry, flag), (div, mult, add)
     if ((leng >= len1-len2) & (lenr >= len2) & (num1(1) == 0) &
         (num2(1) != 0)) # QUOT & REMN arrays have sufficient length and
                           valid numerator & denominator?
          do idxquo=1,leng # clean out QUOT
               quot (idx quo) =0
          do idxrem=1,lenr # clean out REMN
               remn(idxrem)=0
          flag=0 # initialize numerator zero flag
          do idxn1=2,len1 # check numerator for Zero
               if (num1(idxn1) != 0) # non-zero "digit" found?
                    flag=1 # indicate non-zero numerator
                    break # exit DO loop
                    7
               3.
          #
          # could check for Numerator <= Denominator here !!!
               if (Numerator == Denominator)
          #
                    QUOT = 1
                    REMN = 0
               else if (Numerator < Denominator)
          #
                    QUOT = 0
          #
                    REMN = Numerator
               else
                    continue
          if (flag != 0) # non-zero numerator?
               itemp1=num2(1) # byte to integer
               scale=modulo/(itemp1+1) # get normalizing scale factor
               if (scale > 1) # normalization required?
                    carry=0 # initialize CARRY
                    idxn1=len1 # initialize numerator pointer
                    while (idxn1 > 0) # normalize numerator
                                                                 (Continued on page 36)
```

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RSA Cryptography System (Listing continued, text begins on page 16) Listing Four

```
itemp1=num1(idxn1) # byte to integer
     mult=carry+scale*itemp1 # get product
     num1(idxn1)=mod(mult,modulo) # generate new product
                                   # value
     carry=mult/modulo # generate carry for next pass
     idxn1=idxn1-1 # move index to next position
carry=0 # initialize CARRY
     idxn2=len2 # initialize denominator pointer
     while (idxn2 > 0) # normalize denominator
          itemp1=num2(idxn2) # byte to integer
          mult=carry+scale*itemp1 # get product
          num2(idxn2)=mod(mult, modulo) # generate new product
                                         # value
          carry=mult/modulo # generate carry for next pass
          idxn2=idxn2-1 # move index to next position
# else NUM1(1) == 0, so scaling is already done
idxn1=1 # initialize index of NUM1
while (idxn1 <= (len1-len2)) # calculate quotients
     if (num2(1) == num1(idxn1)) # get initial test quotient
          qtest=modulo-1
     else
     itemp1=num1(idxn1); itemp3=num2(1) # byte to integer
     if ((idxn1+1) <= len1) # valid index?
          itemp2=num1(idxn1+1)
     else # no
          itemp2=0
     qtest=(modulo*itemp1+itemp2)/itemp3
itemp2=num1(idxn1); itemp4=num2(1) # byte to integer
if (len2 >= 2) # valid index?
     itemp1=num2(2)
else # no
     itemp1=0
if ((idxn1+1) <= len1) # valid index?
     itemp3=num1(idxn1+1)
     if ((idxn1+2) <= len1) # valid index?
          itemp5=num1(idxn1+2)
     else # no
          itemp5=0
else # no
     1
     itemp3=0
     itemp5=0
while (qtest*itemp1 > (modulo*(modulo*itemp2+itemp3-
         qtest*itemp4)+itemp5))
               # check if test quotient is too large
```

```
qtest=qtest-1
idxn2=len2 # initialize index to NUM2
idx=idxn1+len2 # initialize index to NUM1
borrow=0 # initialize BORROW
while (idx >= idxn1) # perform multiply & subtract
     if (idxn2 > 0) # another "digit" of NUM2?
          itemp1=num2(idxn2) # byte to integer
          borrow=borrow-qtest*itemp1
     itemp1=num1(idx) # byte to integer
     div=borrow+itemp1 # generate mult. & subt.
     if (div < 0) # need to do MODULO's complement?
          borrow=div/modulo # get BORROW for next pass
          div=mod(div, modulo) # MODULO's complement
          if (div < 0) # still need to make DIV positive?
               div=modulo+div # make it positive
               borrow=borrow-1 # adjust borrow
     else # everything is okay
          borrow=0
     num1(idx)=div # update numerator
     idxn2=idxn2-1 # move indices to next position
     idx=idx-1
idxquo=lenq-(len1-len2)+idxn1 # get index to QUOT
if (borrow != 0) # need to add back divisor?
     quot(idxquo)=qtest-1 # adjust quotient
     idxn2=len2 # get index to NUM2
     idx=idxn1+len2 # get index to NUM1
     carry=0 # initialize CARRY
     while (idx >= idxn1) # add back
          if (idxn2 > 0) # another "digit" of NUM2?
               itemp1=num2(idxn2) # byte to integer
               carry=carry+itemp1 # add it back
          itemp1=num1(idx) # byte to integer
          add=carry+itemp1 # add it back in
          if (add >= modulo) # need to adjust ADD?
               add=add-modulo # pull ADD back into range
               carry=1 # set CARRY for next pass
          else # no adjustment
               carry=0 # no carry
          num1(idx)=add # update numerator
```

(Continued on next page)

RSA Cryptography System (Listing continued, text begins on page 16) Listing Four

```
idxn2=idxn2-1 # move indices to next position
              idx=idx-1
         3
              else # no add back needed
                    quot(idxquo)=qtest # generate quotient
               idxn1=idxn1+1 # move index to next position
         idxn1=len1-len2+1 # get index to NUM1
          idxrem=lenr-len2+1 # get index to REMN
          borrow=0 # initialize BORROW
          while (idxn1 <= len1) # unnormalize remainder
               itemp1=num1(idxn1) # byte to integer
               div=itemp1+modulo*borrow # get numerator of REMN
               remn(idxrem)=div/scale # remove normalization
               borrow=mod(div,scale) # get borrow for next pass
               idxn1=idxn1+1 # move indices to next position
               idxrem=idxrem+1
          idxn2=1 # get index to NUM2
          borrow=0 # initialize BORROW
          while (idxn2 <= len2) # unnormalize NUM2
               itemp1=num2(idxn2) # byte to integer
               div=itemp1+modulo*borrow # get numerator of NUM2
               num2(idxn2)=div/scale # remove normalization
               borrow=mod(div,scale) # get borrow for next pass
               idxn2=idxn2+1 # move index to next position
          3
else # cannot generate QUOT & REMN
     call rmrkln('.')
     call remark('Length of [QUOT] or [REMN] too small or .')
     call error('invalid numerator or denominator !!!.')
               # print message and exit
return
```

end

End Listing Four

Listing Five

```
###
                                                ###
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                                                ###
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                                                ###
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                                                ###
                                                ###
# PROGRAM NAME:
           RPEASANT. RAT
# PURPOSE: Russian Peasant exponentiation (re. D.E. Knuth,
```

The Art of Computer Programming, V. 2 (Semi-Numerical Algorithms), 2nd Ed., (Addison-Wesley, Reading, MA), pp. 442-443.) LANGUAGE: RATFOR AUTHOR: CEB USAGE: CALL RPEXP(NUM1, LEN1, NUM2, LEN2, EXPN, LENE, MODL, LENM, WORK, LENW, MODULO) <>NUM* -- Byte array, contains the number [NUM* mod MODL] (byte modulus MODULO) to be operated on. The MSDigit(s) are in NUM*(1) and the LSDigit(s) are in NUM*(LEN*). NUM2 is never modified, but NUM1 is always modified ! >LEN1/2 -- Integer variable, defines the length of the NUM* array. CAUTION: LEN1 = LENM EXPN -- Byte array, contains the exponentiation (byte modulus) MODULO) of [NUM1 ** NUM2 mod MODL]. The MSDigit(s) are in EXPN(1) and the LSDigit(s) are in EXPN(LENE). >LENE -- Integer variable, defines length of EXPN array. CAUTION: LENE = MAX (LEN1, LENM) >MODL -- Byte array, contains the modulus of the arithmetic to be used in the calculations (byte modulus MODULO). The MSDigit(s) are in MODL(1) and the LSDigit(s) are in MODL (LENM) . CAUTION: MODL(1) != 0>LENM -- Integer variable, defines length of MODL array.

(Continued on next page)

Six Times Faster!

○WORK -- Byte array, a working array needed to do modulus arithmetic using MODL, i.e. [? mod MODL].

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RSA Cryptography System (Listing continued, text begins on page 16) Listing Five

```
>LENW -- Integer variable, defines length of WORK array.
#
                        CAUTION: LENW = (LEN2 + 1)
                                                                  <NUM2 temp.>
#
                                          + (LEN1 + LENE) + 1
                                                                  (Numerator)
#
                                          + MAX (LENE - LENM, 1)
                                                                  <Quotient>
#
                                                                   (Remainder)
                                          + LENM
#
               >MODULO -- Integer variable, defines the arithmetic byte modulus
#
                          that is to be used. MODULO has a byte-wide effect
#
                          and should be between 2 and 128 (e.g. 100 for Decimal
#
                          Numbers and 128 for ASCII Characters).
#
                          CAUTION: The Arrays MUST have the same modulus and
#
                          MUST be positive (unsigned) for proper operation !!!
#
# ARRAYS USED: NUM1(*), NUM2(*), EXPN(*), MODL(*), WORK(*)
# EXTERNALS:
              MPMULT, MPDIV
                  INITIAL RELEASE -- 01/20/83 CEB
# UPDATE HISTORY:
subroutine rpexp(num1,len1,num2,len2,expn,lene,mod1,lenm,work,lenw,modulo)
     byte num1(1), num2(1), expn(1), mod1(1), work(1)
     logical maskit, bit, temp, nextf
     integer len1,len2,lene,lenm,lenw,modulo,mask,basmod
     if ((len1 \ge lenm) & (lene \ge maxO(len1.lenm)) &
         (lenw \ge (len2+1)+(len1+lene+1)+maxO(lene-lenm,1)+lenm))
                          # NUM1, EXPN & WORK arrays have sufficient lengths?
          basmod=1 # initialize basic modulus
          repeat # find basic modulus
               basmod=2*basmod # advance basic modulus
          until ((basmod == 128) | (modulo/basmod == 1)) # basic modulus found
          do idxexp=1,lene # initialize EXPN array
               expn(idxexp)=0
          expn(lene)=1 # set EXPN=1
          do idxn2=1,len2 # copy NUM2 to temporary NUM2
               work(idxn2+1)=num2(idxn2)
          work(1)=0 # make sure it starts with zero (for MAKBIN routine)
          idxn2=len2 # get index to NUM2
          nextf=.true. # initialize next pass flag
          while (nextf) # generate Russian Peasant exponential
               call makbin(work,idxwrk,temp,basmod,modulo,len2,idxn2,nextf)
                                                   # make temporary NUM2 binary
               mask=1 # initialize bit mask
               while (mask < basmod) # run through bits within each byte
                                      # of NUM2
                     maskit=mod(mask, 256) # get LSByte of MASK
                    bit=temp & maskit # get appropriate bit of NUM2
                     if (bit != 0) # need to multiply NUM1 by EXPN?
                          call prdmod(expn,lene,num1,len1,mod1,lenm,
                                      work (idxwrk), modulo)
                                    # generate EXPN = (EXPN*NUM1) mod MODL
                     call prdmod(num1,len1,num1,len1,mod1,lenm,work(idxwrk),
                                 modulo) # generate NUM1 = (NUM1*NUM1) mod MODL
                     mask=2*mask # move to next mask bit
```

(Continued on page 42)

| In the second of the second



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RSA Cryptography System (Listing Continued, text begins on page 16) Listing Five

```
idxn2=idxn2-1 # move to next NUM2 Digit
    else # cannot generate Russian Peasant Product
         call rmrkln(".")
         call error('Length of [NUM1], [EXPN] or [WORK] too small !!!.')
             # print message & exit
    return
end
# PRDMOD -- Product Modulo Routine: NUM1 = (NUM1*NUM2) mod MODL
subroutine prdmod(num1,len1,num2,len2,mod1,lenm,work,modulo)
    byte num1(1), num2(1), mod1(1), work(1)
    integer len1, len2, lenm, modulo
    lenp=len1+len2 # initialize length of product of NUM1 & NUM2
    call mpmult(num1,len1,num2,len2,work,lenp,modulo) # multiply NUM1 & NUM2
    if (work(1) != 0) # need to shift result for normalization in Division?
         idxsum=lenp # initialize index to product
         while (idxsum > 0) # shift result down 1 byte
             work(idxsum+1)=work(idxsum)
             idxsum=idxsum-1 # move index to next position
         work(1)=0 # set MSDigit to zero
         lenp=lenp+1 # advance sum length
    lenq=maxO(lenp-lenm,1) # get Quotient length
    idxrem=idxquo+lenq # initialize pointer to start of Remainder array
    call mpdiv(work,lenp,modl,lenm,work(idxquo),lenq,work(idxrem),lenm,modulo)
                  # generate (NUM1*NUM2) mod MODL
    idxend=idxrem+lenm-1 # initialize index to end of remainder
    idxn1=len1 # get index to NUM1
    while (idxend >= idxrem) # remainder digits are available
         num1(idxn1)=work(idxend) # move remainder to NUM1
         idxn1=idxn1-1 # move indices to next position
         idxend=idxend-1
    while (idxn1 > 0) # any Digits left in NUM1
         num1(idxn1)=0 # zero it
         idnx1=idxn1-1 # move index to next position
    return
# MAKBIN -- make sure Temporary NUM2 is binary
```

```
subroutine makbin(work,idxwrk,temp,basmod,modulo,len2,idxn2,nextf)
```

```
byte work(1),bmod
logical temp, nextf
integer idxwrk, basmod, modulo, len2, idxn2
lenw=len2+1 # get length of work
nextf=.false. # initialize next pass flag
if (basmod != modulo) # NUM2 modulus not binary?
     idxquo=lenw+1 # get index to quotient
     leng=len2 # get length of quotient
     idxrem=idxquo+leng # get index to remainder
     bmod=basmod # integer to byte
     call mpdiv(work,lenw,bmod,1,work(idxquo),lenq,work(idxrem),1,modulo)
                                              # make temp. NUM2 binary
     temp=work(idxrem) # get binary temporary NUM2
     idxquo=idxquo+lenq-1 # get index to quotient
     idxwrk=lenw # get index to work
     while (idxquo > lenw) # move quotient to temporary NUM2 for next pass
          if (work(idxquo) != 0) # still digits left?
               nextf=.true. # flag a next pass
          work(idxwrk)=work(idxquo)
          idxquo=idxquo-1 # move indices to next position
          idxwrk=idxwrk-1
     while (idxwrk > 0) # zero out remainder of temporary NUM2
          work(idxwrk)=0
          idxwrk=idxwrk-1 # move index to next position
else # NUM2 modulus is binary
       (idxn2 > 1) # still digits left?
          nextf=.true. # flag a next pass
     temp=work(idxn2+1) # get binary temporary NUM2
idxwrk=lenw+1 # get index to start of unused work area
return
```

end

End Listing



Introduction to PL/C:

Programming Language for Compilers

s LSI densities and chip yields increased steadily, it became apparent that our ability to produce new and more powerful processors exceeded our ability to produce supporting software. An examination of technology advances indicates that hardware gains are due as much to the development of an appropriate tool technology as to the amount of direct resources expended. Efforts in the software domain, however, appear to have been directed much more toward producing immediately usable results than toward advancing the state of the art; the problem is made worse by the high cost of the talent required to implement system software.

One solution, seen by many quite early in the game, was to make software that could be transported from one processor to another in either of two ways: The machine code could be transported, or high-level language source programs could be transported and recompiled for the new machine.

The first approach works well as long as the machine codes remain constant from processor to processor and the destination processor operates in a manner functionally equivalent to the source processor. The primary drawback is a (perhaps unhappy) marriage to a fixed set of operations and instruction formats. The advantages and drawbacks are exemplified by IBM's 360 and 370 series mainframes. That architecture is now over fifteen years old. Its preservation has allowed the transporting of code from model to model, but it has also inhibited technological growth.

The second approach to portability works well provided that the high-level language facility exists for the destination processor and that the high-level language compilers produce functionally equivalent object modules. The primary disadvantage of this approach is that the required

by Morris Dovey

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"It is helpful to think of PL/C as a strictly structured and totally procedural programming language. . . . '

language facility often arrives well after the processor. Also, as a consequence of normal system software development (wherein we keep reinventing the same old wheel), the language processors frequently do not produce functionally equivalent object modules.

The PL/C language has been developed to address the problems inherent in the second approach. PL/C makes possible the implementation of compiler core programs in a processor-independent form, enabling compilers to be developed with much greater speed than before. The PL/C compiler program has itself been implemented in a processorindependent form. Listing One (page 50) is the PL/C listing for the compiler itself. It requires approximately 24 subroutines (a total of about 2K bytes to complete its implementation for any given processor. Listing Two (page 54) is the compiler in macro-assembler code.

PL/C Structural Notation

A PL/C program consists of a sequence of labeled statements, followed by an unlabeled statement consisting of the word "end" (in either upper or lower case), followed by a semicolon, For convenience we will set the end statement aside as a special case and refer to labeled statements simply as "statements."

Statements begin with a label, which may be followed by any number of PL/C specification items, and end with a semicolon. A label consists of a PL/C statement name followed by a colon (or "::="). A statement name consists of a left angle bracket (<), followed by the name, followed by a right angle bracket (>). The name itself may contain imbedded blanks.

within a PL/C statement as a specification item, there must be a statement labeled with the same name. Obviously, syntax definition must continue until all syntactical elements have been resolved to literal values.

Ordering of statements is arbitrary, except that the first statement must be the highest level of definition used. PL/C syntax is fully defined in Table I (page 47).

PL/C Program Structure and Flow

It is helpful to think of PL/C as a strictly structured and totally procedural programming language in which the first statement of any program is the main procedure and all subsequent statements are subprocedures. That way, all syntax item specifications within the body of each statement may be regarded as subprocedure calls. The control flow map of any PL/C program, then, is a tree structure at every node of which is a PL/C specification item.

A node may or may not be classified "output producing." Output may consist of data collected from the input string undergoing compilation, or it may be data generated explicitly by an output specification. The PL/C language provides for the ordered passing of output data up the tree structure from node to node. The output data passed all the way up to the initial node becomes "actual" output: the result of the compilation process. Operationally, then, a compiler programmed in PL/C is not much different from most other compilers. The real differences lie in the development process.

PL/C program flow control is entirely implicit: no language facility exists for explicit transfer of control. Since most programming errors are flow related, this strictly structured approach minimizes many common errors. The problem of attempting to enforce internal structuredprogramming standards is avoided: not only does the language itself make the use of explicit flow control statements seem unnatural, but it also de-emphasizes the entire concept of "flow" by dispensing with the usual notion that control passes from statement to statement in the order in which the statements appear. In addition, the structured approach makes it easy to partition the development effort and removes most of the difficulties involved in follow-on maintenance and enhancement

The BNF (Bakus-Naur Format) description of the target language constitutes the bulk of the compiler code. Because published BNF descriptions are readily available for every major programming language in use, the entire scanning logic for a compiler in effect is available on an "off the shelf" basis. This allows the development effort to be concentrated where it belongs: in the generation of correct and efficient compiler output.

Syntactical Elements

The PL/C programming language is a natural extension of the BNF notation for language syntax definition and description. PL/C extends BNF in three essential ways:

- 1. An output facility has been provided.
- 2. Input collection and symbol manipulation facilities have been added.
- A facility has been included to permit in-line inclusion of instructions and/or data for processing at assembly time.

Some changes of notation from BNF are necessary. Character literals in BNF are normally undelimited; in PL/C they are delimited by single quotes in order to deal with the space character.

The asterisk (*), used to denote the optional occurrence of a specification, is placed following the optional item specification. For example:

'PROC' 'EDURE'*;

indicates that the character string "PROC" is required at the current point in the input file being scanned. Once this requirement is satisfied, the string "EDURE" is permitted but not required. Thus the specification "procedure keyword" may be satisfied by either the string "PROC" or the string "PROCEDURE."

The dollar sign (\$) is used to denote the occurrence of a specification one or more times to satisfy the containing specification. An example of its use might be:

<integer>::= <decimal digit>\$;

where the specification "integer" will be satisfied if "decimal digit" is satisfied one or more times.

The dollar sign and asterisk may be combined to indicate that many optional occurrences of the specification are permitted. For example:

<skip>::= ' '\$*;

will cause the compiler to scan to the next nonblank character in the input stream. If the character at the current point in the input stream is not a space character, the scan will not advance; however, the "skip" specification will OPTIMIZING C86™ controls Charlie...

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not fail since the space(s) are only permitted, not required.

The vertical bar (|) is used to indicate that, should the preceding specification fail, the following specification is to be accepted by the compiler as a valid alternative. For example:

indicates that the specification "arithmetic atom" will be satisfied by the success of either the "constant" specification or the "variable" specification. If the "constant" specification is satisfied, the "variable" specification will not be checked.

All statements are terminated with a semicolon. PL/C programs use a free format, which means that a specification (statement) may extend over as many lines as desired and that several statements may be coded in a single line; in short, line (record) boundaries are ignored.

PL/C Output Facilities

Output specifications in PL/C may occur anywhere within the containing specification and are enclosed in brackets ([]). Three modes of output are available under the current implementation:

Character string output

Hexadecimal value byte output Subordinate node output

Character string output is accomplished by enclosing the string to be generated in single quotes. For example:

will bypass blanks, find the keyword "END" in either upper or lower case, bypass any following blanks, and, if a semicolon follows, output the character string "END" (in upper case, as specified).

Numeric output is accomplished by enclosing one or more pairs of hexadecimal digits in angle brackets, as follows:

 $\langle S370 \text{ SVC Op Code} \rangle := [\langle 0A \rangle];$

Nodal output is accomplished by specification of the subordinate node's position relative to the output specification: where the most recent output-producing node is 1, the next most recent is 2, and so on. In the following example:

if "internal label" is satisfied, the specification will output the string "DC AL4(" and follow it with the output of the "internal label" specification, concluding with the closing right parenthesis. When

multiple nodal output specifications are used together, they must be separated by commas.

Note that, for node numbering purposes, output specifications are not considered to be *output-producing* specifications. Also, for output purposes, a sequence of specifications separated by vertical bars is considered to be a single node.

Assembler Code Insertion and Built-In Functions

Assembler code may be inserted at any point between specifications by enclosing the code in double angle brackets, as follows:

<< Title 'PL/C [V2.0] COMPILER MAINLINE' >>

An assembler instruction may be inserted at any point between specifications in the containing statement by coding an ampersand (&) followed by the assembler op code mnemonic or macro name. If operands are used, they are enclosed in parentheses and must immediately follow the operation mnemonic. For example:

<dummy spec>::=
 ©(DSNAME) &CSECT;

A minimal set of built-in functions has been implemented for the current PL/C compiler using this facility. For example, a SKIP macro calls an assembler language subroutine to advance the scan pointer to the next nonblank character; this approach speeds up the PL/C compiler considerably. Another example is the MEMB macro, which generates a call to another assembler language subroutine to check the character at the current scan point for membership in the character string supplied as an argument to the macro. Thus:

<decimal digit>::=

'0' | '1' | '2' | '3' | '4' | '5' | '6' | '7' | '8' | '9' [1];

can be replaced by the less cumbersome and more rapid:

<decimal digit>:=
 &MEMB('0123456789') [1];

This permits the incorporation of any special facility that may be required either for performance or to meet language requirements (such as attribute analysis for PL/C structure elements). We recommend that this method be reserved for incorporation of new PL/C facilities and that target compiler code be generated using the text inclusion method (angle brackets).

Development History

The PL/C [V1.0] compiler (the initial version) was implemented in Technical Design Labs' Zapple BASIC on a

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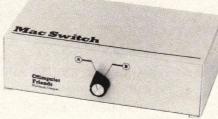
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Z80-based microprocessor system. The entire program required approximately 550 BASIC statements and was adequate only to compile the PL/C compiler itself. The PL/C [V2.0] compiler required only 44 PL/C statements, which was fortunate since the PL/C [V1.0] compiler processed approximately 15 statements per hour.

The output of the BASIC version was a string of macro assembler statements, all of which were macro invocations describing common high-level compiler functions. These macro invocations were generated in a form acceptable to most macro assemblers (as were those of subsequent versions), so the PL/C compiler has existed in a processor-independent form from its earliest stages.

A set of approximately two dozen macros was written to generate Z80 microprocessor instructions. Nearly all of the macros were expanded to compiler subroutine calling sequences, and the appropriate set of compiler subroutines was written in Z80 assembler language. These macros and subroutines are the only processor-dependent code involved in implementing the PL/C compiler.

The common subroutine source code file was concatenated to the generated

compiler mainline file, as was the macro definition file. The composite assembler source file was assembled using Technical Design Labs' macro assembler program.

PL/C [V2.0] was up and running after eight iterations of the process just described. This version was functionally equivalent to PL/C [V1.0] and compiled at a rate of approximately two statements per second — a considerable speed improvement over the original BASIC program! At this point, the PL/C source code was rewritten to provide much greater power and flexibility, additional built-in functions were brought into play, and existing functions were made more flexible.

In generating PL/C [V3.0], which produces Z80 BASIC assembler source code, the macro definitions were maintained in a separate file and were brought into play via an '.INSERT' statement generated as part of a prolog generated by PL/C [V2.1]. The common subroutines were maintained in a separate relocatable object library and were not made a part of the object compiler until link-edit time. This approach permitted separation of compiler components into segments that were much easier to maintain.

PL/C [V3.0] was judged adequate for generation of additional compilers; with this version, we considered the PL/C programming language implementation to be complete. Additional PL/C compiler programming activity is categorized as either maintenance or enhancement.

Availability of PL/C

Readers may obtain a personal-use (non-commercial) PL/C license and Z80-executable versions of PL/C Compilers which generate TDL Assembler and Macro code, the PL/C Subroutine Library in relocatable object form, and PL/C User Manual for \$25. On receipt of payment I will send a license agreement to be signed and returned. On receipt of the license agreement I will mail the diskette. Commented source listings of the PL/C subroutine library (on diskette) and commercial licenses are also available.

DD.

(Listings begin on page 50)

Reader Ballot

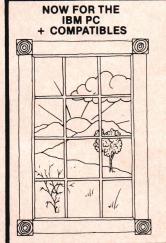
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Table I.

```
(Table 1. Continued)
                          'I' | 'J' | 'K' | 'L' | 'M' | 'N' | 'O' | 'P' |
                          'Q' | 'R' | 'S' | 'T' | 'U' | 'V' | 'W' | 'X' |
                          'Y' | 'Z':
            <alphanumeric>::= <letter> | <digit>;
            <digit>::= '0' | '1' | '2' | '3' | '4' |
                     151 | 161 | 171 | 181 | 191:
            <name segment>::= ' ' '$* <alphanumeric>$;
            <statement body>::= <action spec> <semi> | <statement body>;
            <action spec>::= <generate> | <scan item> | <output item>;
            <scan item>::= <scan call> | <macro call> | <scan string> |
                        <scan call>::= <compiler label> <modifier>*;
           <macro call>::= ' '$* '&' <built in> | <user macro>;
           <user macro>::= <name root> <macro parameter list>*:
           <macro parameter list>::= ' '$* '(' <macro arg>
                                   <addl macro arg>$* ' '$* ')':
            <macro arg>::= <string> | <macro value> | <name root>;
            <string>::= ' '$# '''' \( \string \text{character} \$ '''';

            <string character>::= <alphanumeric> | <special>;
           <special>::= ' ' | '!! | '"! | '#! | '$! | 'g! | '&! | '(' |
                      1)1 | 1#1 | 1:1 | 1=1 | 1-1 | 1{1 | 1}1 | 1[1 |
                       1]1 | 1~1 | 1^1 | 1+1 | 1;1 | 101 | 1|1 | 1\1 |
                      1 1 | 1<1 | 1>1 | 1,1 | 1.1 | 1?1 | 1/1 | 111111 ;
           <macro value>::= ' '$* <digit>$;
           <addl macro arg>::= ' '$* ',' <macro arg>;
           <scan string>::= <string> <modifier>*;
           <scan value>::= ' '$* '#' ''!' <hex pair>$ ' '$* ''!'
                         <modifier>*;
           <hex pair>::= ' '$* <hex digit> <hex digit>;
```

```
<hex digit>::= <digit> | 'A' | 'B' | 'C' | 'D' | 'E' | 'F';
<parenthetic group>::= ' '$* '(' <action spec>$ ' '$* ')'
                     <modifier>#;
<modifier>::= ' '$* <optionally many> | <one or more> |
            <optional> <alt>*;
<optionally many>::= '$' ' '$# '#';
<one or more>::= '$';
<optional>::= '*';
<alt>::= ' '$* '|' <scan item>;
<output item>::= ' '$# '[' <output spec>$ ' '$# ']';
<output spec>::= <node spec> | <output string> | <output value>;
<node spec>::= <node number> ( ' '$* ',' <node number> )$*;
<node number>::= ' '$# <digit>$;
<output string>::= <string>;
<output value>::= ' '$# '<' ' '$# <hex pair>$ ' '$# '>';
<semi>::= ' '$* ';';
<end>::= ' '$# 'END' | 'end' <semi>;
                                                         (Listing begin on page 50)
end;
```



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PL/C Compiler Listing (Text begins on page 44) Listing One

```
<<.TITLE
                'PL/C [2.2] Programming Language for Compilers'
.SBTTL 'Copyright (C) 1981 - MRD Systems, Inc., POB 147. Spring Valley. MN'
.MAIN .:: CALL
                $INIT#
                                 : Initialize Compiler
cprogram>: <statement>$ <end> [2,1];
<statement>: <gen block> | <compiler statement>:
<gen block>: <generate> [1] &FLSH;
<generate>: &SKIP '<<' &COLL(ON) &NEXT('>>') &COLL(OFF) [1]:
<compiler statement>: <statement label> <statement body>
        [2.11
                RTRN
'] &FLSH:
<statement label>: <compiler label> &SKIP ':' ':='* &SYMB(3) ['
; 141
'1':'];
<compiler label>: &SKIP '<' <label root> <label segment>$* &SKIP '>' [3,2];
<label root>: &SKIP &COLL(ON) <letter> <alphanumeric>$* &COLL(OFF) [2,1];
<letter>: <lower case> | <upper case> [1];
<lower case>: &MEMB('abcdefghijklmnopqrstuvwxyz') [1]:
<upper case>: &MEMB('ABCDEFGHIJKLMNOPQRSTUVWXYZ') [1];
<alphanumeric>: <letter> | <digit> [1];
<digit>: &MEMB('0123456789') [1]:
<label segment>: ' ' &SKIP &COLL(ON) <alphanumeric>$ &COLL(OFF) [ ' '1];
<statement body>: <action specification> <semi> | <statement body> [2,1];
<action specification>: <generate> | <scan item> | <output item> [1];
<scan item>: <scan call> | <macro call> | <scan string> | <scan value> |
               <parenthetic group> [1]:
<scan call>: <compiler label> <modifier>* &SYMB(2)
                XFER
                       111.121
                                        ; '3'
1];
<macro call>: &SKIP '&' <built in> | <user macro> [1];
<built in>: <next> | <member> | <collect> [1];
```



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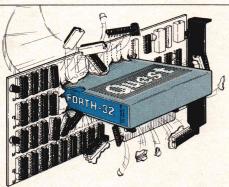
and expiration date)

PL/C Compiler Listing (Listing continued, text begins on page 44) Listing One

```
<next>: 'NEXT' | 'next' &SKIP '(' <string> &SKIP ')' <modifier>*
                NEXT
                         131.111
 1]:
<member>: 'MEMB' | 'memb' &SKIP '(' <string> &SKIP ')' <modifier>*
        [1
                MEMB
                         131.111
1];
<collect>: 'COLL' | 'coll' &SKIP '(' <col on> | <col off> &SKIP ')'
                COLL
                        121
1]:
<col on>: 'ON' | 'on' ['ON'];
<col off>: 'OFF' | 'off' ['OFF'];
<user macro>: <label root> <macro parameter list>* [' '2,1'
1];
<macro parameter list>: &SKIP '(' <macro arg> <addl macro arg>$ * &SKIP ')'
        1
                13,2];
<macro arg>: <string> | <macro value> | <label root> [1];
<string>: &SKIP '''' &COLL(ON) <string character>$ &COLL(OFF) '''' [<60>2<60>];
<string character>: <alphanumeric> | <special> | <string quote> [1];
<special>: &MEMB(' !"#$%&()*:=-{[]}~^+;@|\ <,>.?/
        ') [1]:
<string quote>: &COLL(OFF) ''''' &COLL(ON) ['''']:
<macro value>: &SKIP &COLL(ON) <digit>$ &COLL(OFF) [1];
<addl macro arg>: &SKIP '.' <macro arg> ['.'1];
<scan string>: <string> <modifier>* [' SCAN '2','1'
1];
<scan value>: &SKIP '#' '''' <hex pair>$ &SKIP '''' <modifier>*
                SCNR
                        '<60>3<60>','1'
1];
<hex pair>: &SKIP &COLL(ON) <hex digit> <hex digit> &COLL(OFF) [2,1];
<hex digit>: &MEMB('0123456789ABCDEF') [1];
<parenthetic group>: &SKIP '(' <action specification>$ &SKIP ')' <modifier>*
        &LGEN &LGEN [ * XFER
                               121,131
        GOTO
                111
'2':'5' RTRN
'1':'];
```

```
<parenthetic body>: <action specification> &SKIP ')' | <parenthetic body> [3,1];
<modifier>: &SKIP <alt mod> | <optionally many> | <one or more> |
        <optional> | <default zero> [1]:
<alt mod>: <multi alt> | <single alt> [1];
<multi alt>: '$' &SKIP '|' ['6']:
<single alt>: '|' ['4'];
<optionally many>: '$' &SKIP '*' ['3'];
<one or more>: '$' ['2'];
<optional>: '*' ['1']:
<default zero>: ['0'];
<output item>: &SKIP '[' <output spec>$ &SKIP ']' [2];
<output spec>: <node spec> | <output string> | <output value> [1];
<node spec>: <node number> <addl node number>$# [2,1];
```

(Continued on next page)



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PL/C Compiler Listing (Listing continued, text begins on page 44) Listing One

```
<node number>: &SKIP &COLL(ON) <digit>$ &COLL(OFF)
               NODE '1'
1];
<addl node number>: &SKIP '.' <node number> [1];
<output string>: <string>
              STOP
1];
<output value>: &SKIP '<' &SKIP <hex pair>$ &SKIP '>'
              OHEX
                      1112111
1];
<semi>: &SKIP ';';
<end>: &SKIP 'END' | 'end' <semi>
        [1
                . END
'<1A>];
end;
```

End Listing One

Listing Two

```
.TITLE 'PL/C [2.2] Programming Language for Compilers'
.SBTTL 'Copyright (C) 1981 - MRD Systems, Inc., POB 147, Spring Valley, MN'
.MAIN .:: CALL
                          : Initialize Compiler
                $INIT#
; program
X0001: XFER
                X0002,2
                         ; statement
        XFER
                X0003.0
                               ; end
        NODE
                2
        NODE
                1
        RTRN
   statement
X0002: XFER
                X0004.4
                              ; gen block
       XFER
                X0005,0
                                ; compiler statement
       RTRN
  gen block
X0004: XFER
               X0006,0
                              ; generate
       NODE
       FLSH
       RTRN
; generate
X0006:
        SKIP
                `<<`,0
        SCAN
        COLL
                ON
```

(Continued on page 56)



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PL/C Compiler Listing

Listing Two (Listing continued, text begins on page 44)

NEXT '>>'.0
COLL OFF
NODE 1
RTRN

; compiler statement

X0005: XFER X0007,0 ; statement label
XFER X0008,0 ; statement body
NODE 2
NODE 1
STOP RTRN

FLSH
RTRN

statement label

X0007: XFER X0009,0 ; compiler label SKIP ...0 SCAN `:=`,1 SCAN SYMB STOP 4 NODE STOP NODE STOP RTRN

compiler label

X0009: SKIP `<`.0 SCAN X000A,0 XFER ; label root XFER X000B, 3 ; label segment SKIP '>' .0 SCAN NODE NODE 2 RTRN

; label root

X000A: SKIP COLL ON XFER X000C,0 : letter XFER X000D,3 ; alphanumeric OFF COLL NODE 2 NODE 1 RTRN

```
letter
X000C:
         XFER
                 X000E.4
                                   : lower case
         XFER
                 X000F,0
                                     upper case
         NODE
         RTRN
   lower case
X000E:
        MEMB
                  abcdefghijklmnopgrstuvwxyz 0
         NODE
         RTRN
   upper case
X000F:
        MEMB
                 `ABCDEFGHIJKLMNOPQRSTUVWXYZ`.O
         NODE
                 1
         RTRN
   alphanumeric
X000D:
        XFER
                 X000C,4
                                   ; letter
        XFER
                 X0010.0
                                   ; digit
        NODE
        RTRN
   digit
                 `0123456789`.0
X0010:
        MEMB
        NODE
         RTRN
   label segment
X000B:
        SCAN
         SKIP
         COLL
                 ON
         XFER
                 X000D,2
                                   ; alphanumeric
         COLL
                 OFF
         STOP
         NODE
                 1
         RTRN
   statement body
X0008:
        XFER
                 X0011.0
                                   ; action specification
        XFER
                 X0012.4
                                   : semi
         XFER
                 X0008,0
                                   ; statement body
         NODE
                 2
         NODE
                 1
         RTRN
   action specification
                 X0006,4
X0011:
        XFER
                                   ; generate
```

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27128 300 ns \$22.50 2764 250 ns 8.30 2732 450 ns 4.75 2716 450 ns 3.60 2532 450 ns 4.75 STATIC RAM	6				
2764 250 ns 8.30 2732 450 ns 4.75 2716 450 ns 3.60 2532 450 ns 4.75 STATIC RAM					
2732 450 ns 4.75 2716 450 ns 3.60 2532 450 ns 4.75 STATIC RAM	0				
2716 450 ns 3.60 2532 450 ns 4.75 STATIC RAM	0				
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XFER

XFER

NODE

RTRN

X0013.4

X0014,0

; scan item

; output item

(Continued on next page)

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PL/C Compiler Listing

Listing Two (Listing continued, text begins on page 44)

```
scan item
X0013:
        XFER
                 X0015.4
                                   : scan call
        XFER
                 X0016.4
                                   : macro call
                 X0017,4
                                   : scan string
        XFER
                                   : scan value
        XFER
                 X0018.4
                                   ; parenthetic group
        XFER
                 X0019,0
        NODE
        RTRN
   scan call
                                   ; compiler label
                 X0009.0
X0015:
        XFER
         XFER
                 X001A.1
                                   : modifier
         SYMB
                 2
         STOP
                          XFER
         NODE
         STOP
        NODE
                 2
         STOP
                 3
         NODE
         STOP
         RTRN
: macro call
X0016:
         SKIP
                  . 8. 0
         SCAN
                                    ; built in
         XFER
                  X001B, 4
                                    : user macro
                  X001C,0
         XFER
         NODE
                  1
         RTRN
: built in
                  X001D,4
                                    : next
         XFER
X001B:
                                    ; member
                  X001E.4
         XFER
         XFER
                  X001F,0
                                    : collect
         NODE
         RTRN
   next
X001D:
         SCAN
                  NEXT .4
                  `next`.0
         SCAN
         SKIP
                  ( .0
         SCAN
         XFER
                  X0020.0
                                    ; string
         SKIP
                  ')'.0
         SCAN
                                    ; modifier
         XFER
                  X001A,1
         STOP
                           NEXT
         NODE
         STOP
```

(Continued on page 60)

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PL/C Compiler Listing Listing Two

(Listing continued, text begins on page 44)

```
NODE 1
STOP
```

; member

X001E:	SCAN	'MEMB',4	
	SCAN	`memb`.0	
	SKIP		
	SCAN	`(`.0	
	XFER	X0020.0	; string
	SKIP		
	SCAN	`)`.0	
	XFER	X001A,1	; modifier
	STOP	MEMB	
	NODE	3	
	STOP		
	NODE	1	
	STOP		
	RTRN		

: collect

```
'COLL'.4
X001F:
        SCAN
                 coll .0
        SCAN
        SKIP
                 ('.0
        SCAN
        XFER
                 X0021.4
                                   ; col on
        XFER
                 X0022.0
                                   : col off
        SKIP
                 ')',0
        SCAN
        STOP
                          COLL
        NODE
                 2
        STOP
```

RTRN

col on

SCAN	'ON'.4
SCAN	on .0
STOP	ON'
RTRN	
	SCAN STOP

col off

X0022:	SCAN	OFF.4
	SCAN	off'.0
	STOP	OFF'
	RTRN	

(Continued on page 62)

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PL/C Compiler Listing (Listing continued, text begins on page 44) Listing Two

```
; user macro
                                ; label root
X001C:
       XFER
                X000A,0
                X0023,1
                               ; macro parameter list
        XFER
        STOP
                2
        NODE
        NODE
                1
        STOP
        RTRN
; macro parameter list
        SKIP
X0023:
                ( .0
        SCAN
                                 ; macro arg
        XFER
                X0024,0
        XFER
                X0025.3
                                 ; addl macro arg
        SKIP
                .)..0
        SCAN
        STOP
        NODE
                3
        NODE
                2
        RTRN
; macro arg
X0024:
        XFER
                X0020.4
                                 ; string
        XFER
                X0026,4
                               ; macro value
        XFER
                               ; label root
                X000A,0
        NODE
        RTRN
; string
        SKIP
X0020:
                ... 0
        SCAN
        COLL
                ON
                                 ; string character
        XFER
                X0027,2
        COLL
                OFF
                .,,0
        SCAN
                1601
        OHEX
        NODE
                2
        OHEX
                1601
        RTRN
; string character
                X000D,4
                                ; alphanumeric
X0027:
       XFER
        XFER
                X0028,4
                                ; special
        XFER
                X0029,0
                                ; string quote
        NODE
                1
        RTRN
```

; special

`!"#\$%&()*:=-{[]}~^+;@|\ <,>.?/ X0028: MEMB .0 NODE RTRN

; digit

string quote

X0029: COLL OFF SCAN COLL STOP

RTRN

macro value

X0026: SKIP

COLL ON XFER X0010.2 COLL OFF

NODE RTRN

; addl macro arg

(Continued on next page)



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PL/C Compiler Listing **Listing Two**

(Listing continued, text begins on page 44)

```
X0025:
        SKIP
                 ...0
         SCAN
        XFER
                 X0024.0
                                   ; macro arg
        STOP
        NODE
        RTRN
 scan string
X0017:
        XFER
                 X0020.0
                                   ; string
                                   ; modifier
        XFER
                 X001A, 1
        STOP
                          SCAN
        NODE
                 2
         STOP
        NODE
                 1
         STOP
         RTRN
 scan value
X0018:
        SKIP
                 # .0
         SCAN
                 ...0
         SCAN
        XFER
                 X002A,2
                                   ; hex pair
         SKIP
                 ....
         SCAN
        XFER
                 X001A,1
                                    modifier
         STOP
                          SCNR
         OHEX
                 1601
        NODE
                 3
                 1601
         OHEX
        STOP
        NODE
        STOP
         RTRN
   hex pair
X002A:
        SKIP
         COLL
                 ON
        XFER
                 X002B,0
                                   ; hex digit
        XFER
                 X002B.0
                                   : hex digit
        COLL
                 OFF
        NODE
                 2
        NODE
                 1
         RTRN
 hex digit
```

(Continued on page 66)

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PL/C Compiler Listing (Listing continued, text begins on page 44) Listing Two

```
X002B: MEMB
                 `0123456789ABCDEF`.0
        NODE
                 1
         RTRN
; parenthetic group
X0019:
        SKIP
                 '('.0
        SCAN
        XFER
                 X0011,2
                                  ; action specification
        SKIP
                 ')'.0
        SCAN
        XFER
                 X001A,1
                                  ; modifier
        LGEN
        LGEN
        STOP
                         XFER
        NODE
        STOP
                 3
        NODE
        STOP
        GOTO
        NODE
                 1
        STOP
        NODE
                 2
        STOP
        NODE
                 5
        STOP
                         RTRN
        NODE
        STOP
        RTRN
; parenthetic body
X002C:
        XFER
                X0011,0
                                  ; action specification
        SKIP
                `) `.4
        SCAN
        XFER
                X002C,0
                                  ; parenthetic body
        NODE
                3
        NODE
                1
        RTRN
  modifier
X001A:
        SKIP
        XFER
                 X002D,4
                                  ; alt mod
        XFER
                X002E,4
                                  ; optionally many
        XFER
                X002F,4
                                  ; one or more
        XFER
                X0030.4
                                  ; optional
        XFER
                X0031.0
                                  ; default zero
        NODE
                1
        RTRN
```

```
alt mod
                                  ; multi alt
X002D:
        XFER
                 X0032.4
        XFER.
                 X0033,0
                                  ; single alt
        NODE
        RTRN
  multi alt
X0032:
        SCAN
        SKIP
        SCAN
        STOP
        RTRN
   single alt
                 1,0
X0033:
        SCAN
        STOP
        RTRN
   optionally many
        SCAN
X002E:
        SKIP
        SCAN
        STOP
        RTRN
   one or more
                  $,0
X002F:
        SCAN
         STOP
         RTRN
   optional
         SCAN
X0030:
         STOP
         RTRN
   default zero
                  0
X0031:
         STOP
         RTRN
   output item
X0014:
         SKIP
                  0.1
         SCAN
                  X0034.2
                                 ; output spec
         XFER
         SKIP
                  `]`.0
         SCAN
         NODE
         RTRN
    output spec
                             (Continued on next page)
```

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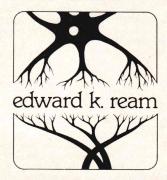
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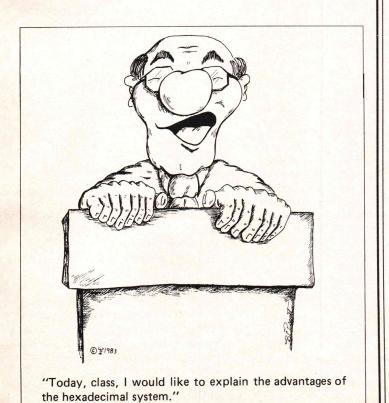
PL/C Compiler Listing Listing Two

(Listing continued, text begins on page 44)

```
X0034:
                  X0035.4
         XFER
                                  ; node spec
         XFER
                  X0036,4
                                  ; output string
         XFER
                  X0037,0
                                  ; output value
         NODE
         RTRN
   node spec
X0035:
        XFER
                 X0038,0
                                 ; node number
        XFER
                 X0039,3
                                 ; addl node number
        NODE
        NODE
                 1
        RTRN
   node number
X0038:
        SKIP
        COLL
                 ON
        XFER
                 X0010.2
                                 ; digit
        COLL
                 OFF
        STOP
                          NODE
        NODE
                 1
        STOP
        RTRN
   addl node number
X0039:
        SKIP
                 ...0
        SCAN
        XFER
                 X0038,0
                                 ; node number
        NODE
        RTRN
  output string
X0036:
        XFER
                 X0020.0
                                   string
        STOP
                          STOP
        NODE
        STOP
        RTRN
```

output value

```
X0037:
         SKIP
                  '<'.0
         SCAN
         SKIP
         XFER
                  X002A,2
                                    ; hex pair
         SKIP
         SCAN
                  '>' .0
         STOP
                           OHEX
         NODE
         STOP
         RTRN
   semi
X0012:
        SKIP
         SCAN
                  `;`,0
         RTRN
   end
X0003:
         SKIP
                  'END',4
         SCAN
                  `end`,0
         SCAN
                                     ; semi
         XFER
                  X0012.0
         STOP
                            . END
         OHEX
                   11A1
         RTRN
                                      End Listing
         . END
```



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Program Design Using Pseudocode

rogramming involves two distinct steps: design and coding. Programmers are often quite adept at coding but deplorably inadequate when it comes to working out the design part.

When you code, your main concern is the language and all the details of program construction: how to pass parameters to the function, how to set up the search loop, or whether the loop condition is checked at its beginning or end. It's something like taking a trip. Which road should we take? When should we stop for gas? What if the Disneyland Hotel has no vacancies?

Designing is analogous to planning the trip. Where shall we go? What shall we do? Shall we drive or fly? Unfortunately, planning the trip, like wrestling with the design of a program, is hardly ever as exciting as actually doing it. It must be human nature to say, "Aw, heck, I'll figure it out as I go." The result is usually pretty chaotic. "Let's see, was INREC 8 bytes or 10 bytes long? I assumed 8 here, but I used 12 over there"

Except for a really trivial program, it is unreasonable to assume that you can design while coding or code while designing. Usually, the design loses out since you tend to concentrate more on coding detail (the how part) than on design detail (the what it does part).

Numerous tools are available for designing programs. Flow charts are probably the most heavily abused of them. Some structured design methodologies, complete with a plethora of rigid rules for their usage, are also available. Then there is pseudo-code.

Pseudo-code is a sort of "fake" programming language, one that is meant to give you lots of flexibility in designing a program. At its worst, pseudo-code can be so restrictive that it becomes much like another programming language. At its best, it allows you to create structures that make sense only to you, although they may be meaningless to a compiler.

In this article, I will present a way of using pseudo-code during the design process. Although I don't intend to set down any coding conventions, I will describe the conventions I used at the end

by Ken Takara

Ken Takara, 1306 Sippola Way, San Jose, CA 95121.

"Pseudocode is a sort of 'fake' programming language, one that is meant to give you lots of flexibility in designing a program."

of the article. Our concern here is the method of usage, not the method of coding.

Design Philosophy

We are going to design a game program. We will start by describing it in very general, rather vague terms. We will then build a modular design using structured English, working from the top down. We will ignore such things as subroutines, variables (for the most part), structures, and primitives. It won't look anything like a program.

Then we will get into pseudo-code. We will translate each module into a routine using pseudo-code, creating more routines as we go. We will also start creating variables, both local and global, and we will reference several data structures. Because of the inexact nature of the design process, we will probably reiterate the various stages several times, reorganizing modules and rebuilding data structures. After that, we'll think about coding.

A Shoot-'Em-Up Game

You are playing a video game. In the center of the screen is the cross hair, against a background of fixed stars. As you move the joystick around, the background moves also. It's as if you were in a spaceship, in the gunner's compartment, scanning nearby space with your missile launcher. A series of fighter craft comes into view! They weave about against the fixed background, and the whole scene moves about the screen as you maneuver the joystick to take aim. When you press the fire button, a missile is launched. Each time a missile intercepts an attacking fighter, you see an explosion and are awarded points. You play until a certain number of ships gets past you, at which point the game ends. The number of fighters on the screen during a "wave" may be limited, as well as the number of missiles on the screen at a time.

Does it sound exciting? No? Not a video game addict, eh? Well, bear with me. The principles we will use to design

this game are applicable elsewhere, too. Let's now do what I call a Level l design specification, using structured English. We may have to rework the design several times, but we should have a decent one by the time we're done.

Level 1 Design

Designing usually requires lots of paper, most of which ends up in the trash. We'll skip the scribbling and go straight to the final draft. Our top down design begins with the main module.

Main Module

- 1. Create attacking fighters and set their initial positions.
- 2. Set initial coordinates of cross hairs against the reference background.
- 3. Draw the initial screen.
- 4. Until the number of fighters that "escape" is greater than the limit, do the following:
 - a. See if any missiles are fired (Fire Control).
 - b. Check for interceptions (Intercept Control).
 - c. Move the fighters (Fighter Movement).
 - d. Move any missiles (Missile Control).
 - e. Determine cross hair position (Scan Control).
 - f. Draw new screen (Display Module).
 - g. Delay for skill level.
 - h. Loop back to 4.
- 5. End of game; display final tally.

Fighter Movement

Given that we know the previous x,y position of each fighter and its range from us:

For each fighter, calculate a new x,y position against the reference background and a new range.

Missile Control

Given that we know the previous

range of the missile from the player (and assuming that its x,y reference coordinates don't change):

Calculate its new range.

Scan Control

Given that we know the previous x,y position of the cross hairs against the reference background:

- 1. Read the x,y values of the joystick and convert to $(\pm 1, \pm 1)$ values.
- 2. Change the x,y values by adding the results from the joystick. This implies that moving the joystick causes the center of the screen to drift against or "scan" the reference background.

Intercept Control

- 1. For each missile:
 - a. Get its x,y position and range.
 - b. Calculate the range it will have after its next missile movement, and call it range 1.
- For each fighter whose x,y position is the same as that of the missile, if missile range ≥ fighter range ≥ range 1:
 - a. Get the point value of the fighter.
 - b. Eliminate the missile and fighter.
 - c. Mark an explosion at the fighter's location.
- 3. Return the total score.

We'll spare you the rest of the modules; suffice to say that Fire Control checks the fire button, and Display Module displays the screen, showing fighters, missiles, explosions, and background stars.

There are quite a few implications and assumptions here. We imply that a data structure exists for each of the fighters, missiles, and explosions. We imply variables for keeping the score and constants to regulate the game speed and number of rounds to play. We assume that we can read the joystick and convert its values and that we can read the fire button.

It's a good idea to sit back and reconsider the game in its entirety just so that we don't become too myopic from staring at the details. For example, notice that Intercept Control checks a missile's next position. The Missile Control module also needs this information in order to move the missile. We might want to calculate the position once then save it for use by other modules. We should make a note of this and modify the design to take this into account. If we noticed this while coding, major surgery might be required on our beautifully written program to accommodate this discovery.

Notice also that, despite all these assumptions and implications, we have not specified how any of this will be done. We simply assume that it can be done. As programmers, we ought to know if it

can be done or not, and we ought to know the limits of the machine. In any event, we have specified what it does without worrying about how it will do it.

At this point, we can easily change our minds concerning the design of the game. We can let the fighters shoot back, or we can describe various classes of fighters with different abilities. We have this flexibility because we have yet to set up the rigid data and control structures.

Once we are satisfied with our base idea, we go on to something closer to a program. We can start using data structures and variables and flow-of-control,

and leave the somewhat ambiguous structured English of Level 1. Figure 4(a) (page 75) presents the conventions adopted for Level 1 design for use as guidelines in subsequent design efforts.

Level 2 Design

During Level 1, we described a set of modules in rather loose terms, implying all sorts of data and control structures, as well as numerous functions and subroutines, without really acknowledging them as such. In Level 2, we transform the modules into routines using real,

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live pseudo-code. Although we use variables, data structures, and certain control structures, we aren't really coding in the normal sense. We are still describing what the routines do but in sufficient detail to make the how of it easy, even obvious, to work out.

We'll start by identifying what might be global variables. These are data structures and other variables that seem to be used in different modules, retaining the changes that other modules make to them. For example, the fighters might be described by a collection of data structures called SHIPREC, while missiles are described by a collection of data structures called MISSILE. Some obvious variables are the total number of points, current center-of-screen coordinates, and the number of fighters that have "escaped."

For Level 2 designs for the Main Module and the Intercept Control routines, we'll again skip the scratchpaper stage and present a (nearly) finished draft. Figure 1 (page 72) shows the logic for the Main Module.

The Main Module routine uses a number of routines, most of which are familiar from Level 1. The notation SHIPREC (i) does not imply that SHIPREC is an array; it is simply a convenient fiction to indicate "this particular SHIPREC" as opposed to "any SHIPREC." We have described only those variables and constants actually used by this routine, although many other global variables exist; we will indicate global variables in other routines as we come to them to keep our list of variables and constants from getting out of hand. We didn't need to describe each of the routines derived from the modules, but we did it for completeness.

We also used two loop structures, one of which looks vaguely like the "do until" loop. The other is easy to understand but looks unlike any "real" loop structure in any language. The two loop structures are actually pseudo-loops, in that they aren't supposed to tell you how to code the loop or even which loop to use. They only tell you what to loop on and when to quit the loop. More on this later.

Of course, the top level module of many programs can be awfully dull; nothing really happens there. All the action is found in the lower level routines. Figure 2 (page 74) shows the pseudo-code for the Intercept Control routine.

The MX, MY, MR, and MR1 variables are local and temporary. Depending upon the language, they may be represented by registers, values on stacks, etc. In some cases, they may never be really created.

The functions, XPOSITION.M, YPO-SITION.M, XPOSITION.S, and so on may be fictitious, depending upon the actual format of the fighter records and missile records. They are created here

Main Module Logic

MAIN.MODULE: routine: for each SHIPREC(i) do (init fighter records) call NEWSHIP (SHIPREC (i)); (create ship record) loop (i); (next rec) FAILURE.COUNT := 0; (failure counter) call DISPLAY, SCREEN; (draw 1st screen) until FAILURE.COUNT=MAXFAIL do (main prog loop) call FIRE.CONTROL: (launch missiles) SCORE := SCORE + INTERCEPT: (Intercept Ctl) call FIGHTER.MOVEMENT; (move fighters) call MISSILE.CONTROL; (move missiles) call SCAN.CONTROL; (new x.v cross hairs) call DISPLAY.SCREEN; (draw new screen) call SPEED.DELAY: (slow game) again; call END.GAME; (final display)

Variables and Constants

SHIPREC

Global structure giving positions of fighters (x,y, range) as well as other information.

SCORE

Global variable with current score.

FAILURE COUNT

end MAIN.MODULE:

Variable giving number of ships that have "escaped,"

MAXFAIL

Constant giving maximum number of failures before game ends. Suggested value = 10.

Functions and Routines

NEWSHIP (ship record)

Initializes a record, creating a new fighter, position, value, etc.

DISPLAY.SCREEN

Draws a screen with current objects (fighters, missiles, explosions, stars, etc.).

FIRE.CONTROL

Performs missile launches.

INTERCEPT

Returns total point value of any fighters shot down by missiles. Checks for interception and eliminates hit fighters and missiles. Records explosions.

FIGHTER.MOVEMENT

Moves each of the fighters.

MISSILE.CONTROL

Moves each of the missiles.

SCAN.CONTROL

Checks joystick and sets new center-of-screen coordinates for cross hairs.

SPEED.DELAY

Slows down the action by inserting a delay.

END.GAME

Performs end-of-game cleanup and display.

Figure 1.

Intercept Routine Logic

```
INTERCEPT: Function;
POINTVAL := 0:
                                              (initialize point count)
for each (MISSILE(i)) do
                                              (another loop)
    MX := MISSILE(i).XPOS;
                                              (current x coordinate)
    MY := MISSILE(i).YPOS;
                                              (current v coordinate)
    MR := MISSILE(i).RANGE;
                                              (range from player)
    MR1 := MR + MISSILE(i) .SPEED
                                              (find next position)
    for each (SHIPREC(i)) do
                                              (check each target ship)
        if SHIPREC(j) .XPOS=MX
                                              (is fighter at the same . . . )
        and SHIPREC(j) .YPOS=MY
                                              (position as missile?)
            if MR≤SHIPREC(j).RANGE≤MR1 then
                                                                (a hit!)
                POINTVAL := POINTVAL + SHIPREC(i) .VALUE
                call ELIMINATE (MISSILE (i));
                call NEWSHIP (SHIPREC(i));
                exitloop(i);
                                              (exit SHIPREC loop)
            endif
        endif:
    next(j);
next(i);
return (POINTVAL);
```

Variables and Constants

POINTVAL

Sum of points added this turn.

MX, MY, MR, MR1

Holding variables for missile position (x,y,range) and projected range of missile. SHIPREC

Global data structure describing fighters. Includes position and value information. See description below.

MISSILE

Global structure for missiles. See description below.

MISSILE and SHIPREC Structures

MISSILE (record) .XPOS X position of a missile. MISSILE (record) . YPOS Y position of a missile. MISSILE (record) .RANGE Distance of a missile from the player. MISSILE (record) .SPEED Velocity at which a missile is traveling.

SHIPREC (record) .XPOS X position of an attacking fighter craft. SHIPREC (record). YPOS

Y position of a fighter.

SHIPREC (record) . RANGE

Distance from player to attacking fighter.

SHIPREC (record). VALUE Point value of a target fighter.

SHIPREC (record).

Functions and Subroutines

ELIMINATE (missile record)

Deletes a missile record (either by unlinking it, if a linked list is used, or by setting an "unused" flag).

NEWSHIP (ship record)

Replaces a destroyed fighter with a new one.

NEWBANG (x,y,range)

Makes an entry in the explosion table for the display.

Figure 2.

for convenience. The explicit difference between XPOSITION.M and XPOSI-TION.S, and between YPOSITION.M and YPOSITION.S, and so on, is because we don't know anything about the actual layouts of the missile and fighter records.

The routine NEWSHIP has been re-used, indicating that it may be global or at least defined at a higher level than this routine. When designing and coding it, we will have to keep in mind that it can be called at any time, not only during the initial setup.

Although we have identified some of the fields within the fighter and missile records, we still do not impose a format on them. We may find that we need more fields in some of these records. For these reasons, we reference the fields within the records by name, using the "dot" convention - RECORD.FIELD1 refers to a field called FIELD1 in a data structure called RECORD.

In both of the routines that we pseudo-coded, we referred to the fighter and missile records with miniscule subscripts. This is not intended to imply that they are arrays. We have not specified what their actual structure is and simply use the miniscule subscript to single out a particular record and to imply an ordering to the records. That is, some "first" record exists and for any record a "next" record exists. These records could be arrays, or they could be elements in a linked list or sequential blocks in memory. The physical layout is not important at this time.

Figure 4(b) (page 75) lists the conventions used for Level 2 design.

Loops and Other Control Structures

A dozen different loop constructs no doubt are available among programming languages, as well as a dozen ways to use them with varying degrees of effectiveness. If you use an "until" loop, does it check the condition at the beginning of the loop or at the end? What is the value of the loop counter after the loop terminates? Is the loop always executed at least once?

Many of the loop peculiarities depend upon the implementation. We would like to have a set of pseudo-loops that always does what we want it to do. Since we are working with pseudo-code, there is no reason why we can't create such loops. Let's assume that our loops will be executed zero times if the exit condition is met immediately upon entry to the loop. Let's also assume that loop counters retain their values after the loop is exited. We know that we can ensure these two points during real coding, even if the language doesn't support them, by adding our own checks or by using different loop constructs.

Three things are common to loops.

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First, they assume some initial condition or initial item: the initial value of a counter, for example, or the first element in a linked list. Second, most loops assume that a "next" item follows the first. Third, the program can exit the loop either when the elements are exhausted or upon realization of an "exit" condition of some sort. Regardless of the peculiarities of any language, loop structures almost always have provision for these three points. Of course, we may have to do some twisting to use them the way we want to, but that is why we work out the design in pseudo-code to begin with.

Given that we can have our pseudoloops do what we want them to do, what sort of loop constructs might we use? We suggest the following types:

The "until X do . . . again" loop. If condition X exists upon entry to the loop, the loop is not executed.

- "while X do . . . again" is similar to (1) but runs while condition X holds. If X is not true on entry, the loop is not executed.
- "for each X do ... loop" or "for each X (i) (for i=m to n) do . . . loop" runs the loop until the elements X are exhausted.
- "do . . . if X then exit . . . again" exits the loop upon satisfaction of the test.

Two other control structures that are frequently used are the "if . . then . . else . . endif" and the "case . . of . . endcase" structures. Everyone knows the first structure. We like to close each "if" with an "endif." You may or may not, depending on how you feel. The "case... of .. endcase" structure may be thought of as a selection of one out of several subroutines. For example:

Structure

offset	name	size	description
+0	NEXTREC	2 byte	Pointer to next record (linked list)
+2	XPOS	2 byte	X coordinate of record
+4	YPOS	2 byte	Y coordinate of record
+6	RANGE	2 byte	Distance from player

New Fighter Routine

Point value of ship

NEWSHIP (SHIP): Routine:

VALUE

SHIP.XPOS := RANDOM(0,255);(random X coordinate) SHIP. YPOS := RANDOM (0,255); (random Y coordinate)

SHIP.RANGE := RANDOM (MAXRANGE*3/2, MAXRANGE);

2 byte

(random distance)

SHIP. VALUE := 100*RANDOM(1,3);

(random value)

end NEWSHIP:

SHIPREC

+8

Variables and Constants

SHIP

Local reference to a SHIPREC record.

MAXRANGE

Constant giving maximum start distance for attacking fighter.

Functions and Routines

RANDOM (m,n)

Returns a random number between m and n.

Figure 3.

case (I) of

- (1) DOTHIS;
- (2) DOTHAT; else DOTHEOTHER;

endcase

performs the routine DOTHIS if the selector, I, is 1; it does DOTHAT if I=2; and it performs DOTHEOTHER for any other value of I. It is easy to implement even in languages lacking the "case" structure.

The control structures presented here should be sufficient to make the intended structure of any program clear. Often their actual implementations during coding become obscure due to language peculiarities, but a quick reference to the pseudo-code version should serve to remind you of the intended structure. Thus, you will not lose the design of your program while you are struggling with the problems of coding.

Routines and Functions

We started the design process with a loose description of what we wanted to do. We then formalized it somewhat during Level 1 by breaking the process into modules, written in structured English. From there we went on to Level 2, where we transformed the structured English to pseudo-code. At each step we assumed and used numerous subroutines and functions. So where do we describe the subroutines and functions?

There are three classes of routines. One class of routines includes those described as Level 1 modules; these, of course, we translated to Level 2 routines. We briefly described the next class of routines in the "Functions and Subroutines" part of the figures, some of them with sufficient detail to go on to pseudo-coding them. The last set are those routines that are sufficiently complex to require whole Level 1 descrip-

tions of their own. For example, a sophisticated parsing routine within another program might be sufficiently complicated to justify having its own thorough design.

By this time, you are probably pretty familiar with your design, maybe even a little sick of it. It might be reasonable to try coding a small test program to see how it looks. You may discover something that you hadn't anticipated that impacts on your design. For example, the method you selected to draw the display might prove to be far too slow. You experiment to find a faster way, but it involves a major design change. Oh well, it's better to know now before you have logged a hundred hours of coding and debugging time.

In designing a program, you should defer "programmer" problems until the final stages. But before you start the last design step, it definitely helps to consult

These are the conventions used for the various design levels in this article. They should be taken as guidelines for working out the design and are not meant to be strictly adhered to.

(a) Level 1 Conventions

Modules are worked out from the top down.

Each module is laid out in an outline format.

A control structure encompasses the lines that are indented under it (control structures usually end with an ending keyword).

Variables and data structures are only implied.

Entries should describe actions performed without referring to the "how it's done" detail.

All major activities ought to be covered, and the modules should be worked out such that all relevant cases are covered.

If any single module becomes too cumbersome, it should be redesigned or broken into smaller modules.

(b) Level 2 Conventions

Each module created during Level 1 design should be included as a routine or function. All nonprimitive routines and functions used during Level 2 design should also be pseudo-coded.

Each section of pseudo-code (each routine) should include a list of variables and constants and a list of routines and functions that were used. This provides a cross reference.

Variables and data structures should be marked as "global" if they are used in more than one routine. The actual assignment of variables to specific routines may be done during Level 3.

Variables, constants, functions, and routines are written in upper case to make them distinguishable from control words.

Semicolons are used to separate "statements" from each other. There are no rigid rules however, for statement separation.

Use nested structures. They are much cleaner than branches. Indent the lines for readability.

Use miniscule letters to indicate specific elements in data structures. They may resemble arrays, but they make it easier to specify particular elements of lists. "ELEMENT-(i)" is an element of a list of elements, regardless of its actual format. "ELEMENT(I)" is an obvious array.

If you want to exit a loop prematurely, use generalized loop structures, such as:

"for each ITEM(i) do . . . next (i)"

"until CONDITION do . . . again"

"while CONDITION do . . . again"

"do (i) . . if CONDITION then exit (i) endif . . . again".

Instead of the "goto SOMEWHERE" structure, use the two conditional execution structures:

"if CONDITION then . . . else . . . endif"

"case (I) of (1) SUB1; else SUBn endcase"

(The "goto" is used only while coding, when the language lacks the sophisticated block structures or loop "exit" words.)

(c) Level 3 Conventions

The pseudo-code looks just like Level 2, except that data structures and variables are defined according to language and machine limitations.

Pointers can be initialized using the ADDR(X) function; "PTR1 := ADDR(X)" is usually pretty clear.

Reference to a location via a pointer can be coded as [PTR1]. For example, "[PTR1] := X" copies the value of X to the location reference by PTR1.

Language-supplied constructs may be used; however, it is a good idea to limit these, as this is still part of the design phase.

Figure 4.

the computer. What you learn by building a small test system will help as you struggle with the Level 3 design. Machine dependencies have a way of wrecking "machine-independent" designs just as easily as ignorance of the design can mangle your tidy code. As designer and programmer, you catch it from both sides.

Level 3 Design

We have now pseudo-coded most of the routines in our program. We know what the modules are and what they are to do. We also know about a whole mass of variables and data structures. Now we need to know what the data structures look like and how they are accessed. At Level 3, we design the data structures and the primitive operations on them.

At this point, we become concerned with the selection of the language, as well as language and machine dependencies. We should restrict ourselves, however, to dependencies relative to data structures. We will still use the techniques of Level 2 for specifying control structures and avoid implementation dependencies for these as yet; although we must address the dependencies regarding data structures, we are

still involved with design.

For this example, let's pseudo-code the NEWSHIP routine. We must also specify the layout of the fighter record SHIPREC with respect to some language. Let's assume that the language we are using permits us to reference the record and its fields by specifying a name and and offset. We first describe SHIPREC in Figure 3 (page 74).

We have specified, for purposes not connected with the NEWSHIP routine, to include a "next record" pointer in the fighter record, implying that the fighter records constitute a linked list. We have also referenced a random number generator, which we hope is at least partially supplied by the language we intend to use.

Eventually, we will have all the routines pseudo-coded in a similar manner. Figure 4 (c) (page 75) presents the Level 3 design conventions. Assuming that we are still satisfied with our design, we can go on to the final step: coding.

From Pseudo-Code to Real Code

Once we decide to start coding, we should put all our energy to writing code,

trusting that our design is complete. The actual process of coding, of course, depends upon the language we have selected. However, we might follow certain general procedures. When we embarked upon design, we worked from the top down. When coding, we work from the bottom up except for the variables, which usually must be declared prior to their use.

Generally, the transformation from pseudo-code to real code is pretty painless, for an excellent reason: you have made all the global decisions and can now concentrate on the local, line-by-line ones of coding! The loop structures, however, always seem to have it in for us. Somehow, loops always do something other than what we want them to do, forcing us to find some way of getting them to do our bidding.

As we mentioned before, most loops imply the existence of an initial element, a successor element, a final element, and some exit method. In coding a loop, then, we should find the most effective way of specifying these points. For example, in our pseudo-code we might have said, "for each SHIPREC(i) do.." Instead of "for I=1 to n" this might translate to:

Table of Terms

primitive

A variety of subroutine or function that performs some very basic job within the context of your program. Disk access words, I/O routines, and data structure access words might be considered primitives. Of course, if you are writing a disk access package, you might use other routines that are even more primitive.

case statement

Something like an "indexed goto," where you execute one of several possible blocks of code depending on a selector variable.

loop structure

Any program control structure that allows a block of code to be executed repeatedly. This includes such things as "TOP: (some code) GOTO TOP;" that executes the code between "TOP" and the "GOTO TOP" statement.

global variable

Technically, a variable that may be directly accessed by name from all routines in a program. For our purposes, a variable that can be accessed

by name from more than one routine. In block-structured languages, the degree of "globalness" of a variable depends upon the level of the routine within which it is defined.

data structure

Something like a collection of variables. Common data structures are: arrays, records, tables, linked lists, or files. The last type, however, usually implies disk-resident data.

module

A group of activities that can be taken together as a cohesive set. For our purposes, one of the functional subdivisions that helps describe the activity performed by the program. Modules interact with each other under the supervision of a main module.

top down design

A method of analyzing the design from the most general level, through increasing degrees of detail, until some bottom level of most intimate detail is reached. In our case, three levels of complexity occur before actual coding is reached, and most of the analysis occurs during the second level.

bottom up

The method of reconstructing the program from the lowest level of routines (the primitives) on up through the major modules. You analyze from the top down and build from the bottom up.

pseudo-code

A "fake" language designed to aid in the design process. It does what you want it to do, rather than forcing you to contort your thinking to fit its limitations.

tight (structured) English

A method of specifying an activity in a readable form. Rules for games or instructions for procedures to be followed are often written in structured English. It should get the idea across to a person, though it may not make much sense to most compilers. When you write using structured English, pretend that you are writing down a set of rules for someone to follow (as if they will be doing the activities of your program by hand).

PTR.S := FIRST.SHIPREC; until PTR.S=0 do . . . PTR.S := PTR.S+0 again

Summary

The design process begins with an informal description of the program in terms of its actions. This is separated into modules, each of which is stated in structured English. The focus is on what the modules do, and on which one does what, rather than on how it is done. During this Level l design process, data structures are implied by the actions being performed and are not explicitly described.

During Level 2 design, the tight English modules are converted into routines and functions composed of calls to other routines, held together by control structures. The control structures mirror the various structures available in most languages, but they are assumed to do what we want them to do rather than what the language designer specified they should do. Loops check conditions upon

entry and may be skipped altogether if the exit conditions are already in effect. The data structures and variables implied during Level 1 are stated, but their actual formats are left vague. Data types are kept simple; integers, real numbers, pointers, characters, and strings are commonly used. You should feel free, however, to create any new data types you want.

Level 3 design starts by describing the data structures and variables used during Level 2 design. Language and machine dependencies should be taken into account at this point. Primitives that work on the data structures are pseudo-coded, still describing what they do, except that now they are designed with the language and machine in mind. We may also wish to annotate routines created during Level 2 design if they happen to directly manipulate any of the data structures.

Finally, the pseudo-coded routines are translated to the actual program code. The various routines are laid out as skeletal forms, and the variable declarations are coded. The routines are coded starting with the primitives, working up to the higher level routines. Routines should be tested as they are coded to simplify the problems associated with integration.

The transformation of loop structures, "if.. then.." structures, and "case.." structures should be watched, as language peculiarities can cause difficulties.

If you use this method conscientiously, you should be able to work out the design of a program before you get entangled in the details of coding. Coding itself should be much easier since you can ignore design considerations at that point. Working from Level 2 and Level 3 pseudocode, even if you are working with the most primitive and unstructured of languages, you should be able to see the program structure easily by comparing the real code with the pseudo-code. Program documentation should be easier, too, as the pseudo-code provides the basis for program comments.



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More on Binary Magic Numbers

dwin E. Freed presented some valuable information in his article "Binary Magic Numbers" (DDJ No. 78). Not only are the magic numbers useful, but the algorithms show how the parallel processing capabilities of even the simplest computer (in this case, the ability to process all of the bits of a "word" in parallel rather than one at a time) can be used to dramatically improve the execution time of a program.

He chose Pascal, however, to present his algorithms. I do not want get involved with the unsolvable debate about "Which is the best programming lan-

by Dale Wilson

Dale Wilson, Codewright, 231 Couch Ave., St. Louis, MO 63122. guage?" but in this case I can confidently state that C is a better choice for the programs in the article.

The primary reason is that C has operators which map directly into machine operators, so there is no need to invent functions to mimic the hardware. The use of the operators &, |, \, \, \, \, << and >> which stand for AND, OR, EXCLUSIVE OR, COMPLEMENT, SHIFT LEFT, and SHIFT RIGHT, makes a C version of the program much more straightforward (at least to someone who can read C). The "op=" and "++" and "——" operators of C also correspond to the instructions available in assembly language, so I have used them.

In fact, in translating the programs to C in order to understand them more clearly, I was able to come up with cleaner versions of several of the functions presented by Freed. In most cases, I used a

more natural index to control the loops. It also turned out to be useful to split the array B into two parts. B1 is the first half of the original array — the binary magic numbers. B2 is the second half — the complement of those numbers.

The resulting functions are shown in the listing (below). I hope this will direct the attention of people back to the ideas in Freed's article. You may never need to hand-code a parity function or a side-sum function, but someday you may need to sort or sum a matrix on a multiprocessor computer, and these or some other binary magic numbers may be just the trick you need in order to complete the task in log time rather than linear time.

DD

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Binary Magic Listing

```
"Binary Magic Numbers" by Edwin E. Freed in DDJ #78. April 1983.
     by Dale Wilson, 1983
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 * These functions were written so they may be directly translated
  into assembly language for most computers.
 * These functions were tested on a Zenith 100 computer using the
 * C86 compiler from Computer Innovations, Inc.
 */
#include <stdio.h>
#define TRUE 1 /# stranger than fiction #/
#define FALSE 0 /* fiction */
#define CNTLZ 26 /* MS-DOS eof */
               /* bits per "word" */
#define N 16
#define V 4
               /* log 2 of N */
/# Since C does not have binary constants, the binary magic numbers are
 * expressed below in hexadecimal. B from Freed's article is divided
 * into b1 and b2 since there was no good reason to have them in the
 * same array.
 */
unsigned int b1[V] = \{ 0x5555, 0x3333, 0x0F0F, 0x00FF \};
unsigned int b2[V] = { OxAAAA, OxCCCC, OxFOFO, OxFFOO };
```

/* Some useful bit manipulation functions inspired by the article

```
/* converting binary numbers to Gray code is so simple that it may
 * be best defined as a macro rather than a function.
 */
#define binary_to_Gray(x) (x ^ x >> 1) /* X exclusive_or X shifted_right 1 */
/* MAIN routine to test the functions.
 * Numbers (entered in hexadecimal) will be used as arguments in each
 * of the functions. As an additional check, the binary number resulting
   from the Gray_to_binary function will be converted back to Gray code--
   which should result in the original number.
 */
                       int argc; char *argv[];
main(argc, argv)
       unsigned int r,i;
       int c;
       while (TRUE)
                                                /# loop forever #/
               printf("Enter number: "):
                                                /* read a hexadecimal */
               fscanf(stdin, "%x", &i);
               while((c=getchar()) != '\n')
                                               /* discard rest of line */
                       if(c == EOF !! c == CNTLZ) /* except on end of file */
                               exit():
                                                /*
                                                    quit */
               printf("low clear bit: %d\n", low_clear_bit(i));
               printf("high set bit : %d\n", hi_set_bit(i));
                                    : %d\n", side_sum(i));
               printf("side sum
               printf("parity
                                    : %d\n", parity(i));
               r = Gray_to_binary(i);
                                    : 0x%04x\n", r);
               printf("Gray code
               printf("GTB To Binary: 0x%04x\n", binary_to_Gray(r));
               printf("Reversed bits: 0x%04x\n", reverse_bits(i));
               putchar ('\n');
                                                /* leave a blank line between */
       3
/* This function returns the bit number of the lowest bit in the word
 * which is clear (zero). The least significant bit is numbered 0, the
 * bit to the left of that, 1, and so on...
 * A minus 1 is returned for words in which all bits are on.
 * The time to execute this function is proportional to V which is
 * log2 of the number of bits in a word.
 * Note that the function low_set_bit may be created by complementing the
 * argument and calling low_clear_bit.
 */
low_clear_bit(value)
                       unsigned int value;
       unsigned int temp;
       int i, result;
       if((value = ~value) == 0)
                                                /# complement, test for zero #/
                                                /# zero means no clear bits #/
               result = -1:
       else
               result = 0;
               for (i = V-1; i >= 0; i--)
                                                /* make space for next bit */
                       result <<= 1;
                                                /* test using magic numbers */
                       temp = value & b1[i];
                       if(temp == 0)
                                                /* next bit of result is 1 */
                               result != 1;
                       el 5e
                                                /* discard disqualified bits */
                                value = temp;
               3
       3
                                                                (Continued on next page)
```

Binary Magic Listing (Listing continued, text begins on page 78)

```
return (result);
3
/# This function returns the bit number of the highest bit in the word
 * which is set (one). The least significant bit is numbered 0, the
 * bit to the left of that, 1, and so on...
 * A minus 1 is returned for words in which all bits are off.
 * The time to execute this function is proportional to V which is
 * log2 of the number of bits in a word.
* Note that the function high_clear_bit may be created by complementing the
 * argument and calling high_set_bit.
 */
hi_set_bit(value)
                      unsigned int value;
       unsigned int temp:
       int result, i;
       if(value == 0)
                                               /# if no bits are on #/
                                               /# return that info #/
               result = -1;
       else
               result = 0;
               for (i = V-1; i >= 0; i--)
                       result <<= 1;
                                               /# space for next bit #/
                       temp = value & b2[i];
                       if(temp != 0)
                                               /* next bit is one */
                               result != 1;
                               value = temp; /# discarded unneeded bits #/
                       3
               3
       return(result);
3
/* This function returns a count of the number of bits which are on in a
 * word. It executes in a time proportional to V, the log base 2 of the
 * number of bits in a word.
 * Note that a count of the number of zero bits in the word may be found
 * by complementing the value and calling side_sum.
 */
side_sum(value)
                   unsigned int value;
}
       int i;
       unsigned int s:
       5 = 1:
       for (i=0: i<V: i++)
                           /* use magic in reverse order */
               value = (value & b1[i]) + ((value >> s) & b1[i]);
                               /* generate the powers of two on the fly */
       return(value);
/# This function converts a number expressed in Gray code to the
 * equivalent binary number. It executes in time proportional to the
 * log base 2 of the number of bits in the word.
 */
Gray_to_binary(value) unsigned int value;
        unsigned int s;
       for (s = N >> 1; s != 0; s >>= 1)
               value ^= value >> s;
```



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Binary Magic Listing (Listing continued, text begins on page 78)

```
return(value);
/# This function returns the parity of a word--that is, it returns a zero
* if the number of one bits in the word is even, and a one if the number
 * of one bits in the word is odd. The low order bit of the results of
 * Gray_to_binary and side_sum both have this property, so isolating this
 * bit gives the desired result. Gray_to_binary was selected since it is
 * a faster function than side_sum.
 */
parity(value)
                      unsigned int value;
       return(Gray to binary(value) & 1); /* build on previous work */
/* This function reverses the bits in a word. Strangly enough, this turns
 * out to be a very useful function to have available. Note that this function
 * works only on functions with word lengths which are powers of 2.
 */
                       unsigned int value;
reverse bits(value)
       unsigned int s,i;
                               /* s provides the powers of 2 */
       5 = 1:
       for (i=0; i<V; i++)
               value = ((value << s) & b2[i]) ! ((value >> s) & b1[i]);
               s <<= 1:
       return(value);
                                                                       End Listing
3
```



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high set bit: 0
side sum : 1
parity : 1
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GTB To Binary: 0x0001
Reversed bits: 0x8000

Enter number: 0002
low clear bit: 0
high set bit: 1
side sum : 1
parity : 1
Gray code : 0x0003
GTB To Binary: 0x0002
Reversed bits: 0x4000

Enter number: 0003
low clear bit: 2
high set bit: 1
side sum : 2
parity : 0
Gray code : 0x0002
GTB To Binary: 0x0003
Reversed bits: 0x000

Enter number : 0004
low clear bit: 0
high set bit : 2
side sum : 1
parity : 1
Gray code : 0x0007
GTB To Binary: 0x0004
Reversed bits: 0x2000

Enter number: 8000
low clear bit: 0
high set bit: 15
side sum : 1
parity : 1
Gray code : 0xFFFF
GTB To Binary: 0x8000
Reversed bits: 0x0001

Enter number: 7FFF
low clear bit: 15
high set bit: 14
side sum: 15
parity: 1
Gray code: 0x5555
GTB To Binary: 0x7FFF
Reversed bits: 0xFFFE

Enter number: FFFF
low clear bit: -1
high set bit: 15
side sum : 16
parity : 0
Gray code : 0xAAAA
GTB To Binary: 0xFFFF
Reversed bits: 0xFFFF

Enter number : 1111 low clear bit: 1

Enter number : 0000 low clear bit: 0 high set bit : -1 side sum parity : 0 Gray code : 0x0000 GTB To Binary: 0x0000 Reversed bits: 0x0000 high set bit : 12 side sum parity 0 . Gray code : Ox1E1E GTB To Binary: 0x1111 Reversed bits: 0x8888

Enter number: 3333
low clear bit: 2
high set bit: 13
side sum : 8
parity : 0
Gray code : 0x2222
GTB To Binary: 0x3333
Reversed bits: 0xCCCC

Enter number: 7777
low clear bit: 3
high set bit: 14
side sum : 12
parity : 0
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GTB To Binary: 0x7777
Reversed bits: 0xEEEE

Enter number :

End Sample Run

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SOFTWARE REVIEWS

SXR PLUS: Sorted Cross Reference Company: Prasek Computer Systems, Inc., Box 2427, Santa Clara, CA 95055

Computer: Apple II/II+ (48K RAM) or IIe with DOS 3.3 and Applesoft One disk drive; Optional (but strongly recommended): Printer, 80-column card, second disk drive

Price: \$39.95
Circle Reader Service No. 133
Reviewed by Charles Petzold

SXR PLUS is a handy utility for programmers who need a cross-referenced index of variables in an Applesoft program. It is especially useful when debugging or modifying a program.

SXR PLUS provides 40- or 80-column output to a screen or printer. Output may be tailored to include or exclude line number references, numeric constants, and/or quoted literals (strings). A search feature is included to find specific variables. Special instructions are provided for programmers who want to move DOS into the upper 16K of an Apple IIe or to a 16K RAM card on an Apple II+.

Complete instructions are contained in a 24-page booklet. It's slow going the first time through because Prasek takes great pains to make sure you understand every step. After that first run-through, however, the procedure is so simple the manual isn't needed unless you start having problems. If that happens, a troubleshooting guide is included to put you back on track.

When SXR PLUS is booted, a menudriven program is used to set initial parameters. You must decide whether you want line number references, numeric constants, and/or quoted strings included in the cross-referenced list. If you choose standard video output (a monitor or TV set), 40-column width is automatic. If you choose another output device (such as a printer), you must select the proper slot number and determine whether you want 40-column or 80-column output. The output will pause every 20 lines if you select the pause option.

Once the parameters are set, the SXR PLUS diskette is removed from drive 1 and replaced with the disk containing the program to be cross referenced. After this program is loaded, the disk is removed and the SXR PLUS disk reinserted. To get a cross reference, BRUN SXR PLUS and select either F for a full sort (as determined by the parameters) or S to search

for a specific variable. That's about all there is to it.

SXR PLUS comes in handy when you're debugging. Good programmers keep a list of every variable they use. If you make a typo and accidentally use BA\$ instead of AB\$, BA\$ will show up in a sorted cross reference, and you will have found the error and the line number where the mistake was made.

Another use for SXR PLUS is when you decide for one reason or another to change a variable. If you don't change every occurrence, you've got trouble. Using the search function, you'll be able to find (and later change) every occurrence.

Depending on the complexity of the program, a sort may take only seconds or it may take several minutes. For test purposes, I used the 47-sector LEMONADE program from Apple; a full sort took more than six minutes. I also tried a 91-sector program (PLANET.OF.THE.ROBOTS on a Softdisk Magazette), and a full sort took more than eight minutes. Both sorts were viewed on the monitor, not a printer.

Although SXR PLUS did everything it promised, there were some minor annoyances. To set the initial parameters, for example, you must answer six or eight questions, depending on your answers. After answering all questions, you are asked if the parms are correct. If they are not, you must repeat your answers to all questions. It would be quicker and easier to change only those answers that need correction (or updating). This can be easily fixed with some minor programming changes.

Another nice feature would be default parameters, with the default option being the option the user would most often select. To accept the default option, the user would merely hit the RETURN key.

Also, no provision is made for a twodrive system. With some more minor programming changes, this could be added. It would eliminate the disk-switching required under the one-drive system.

Since SXR PLUS is not on a protected disk, experienced programmers will no doubt be quick to make the necessary modifications.

With respect to copies and distribution, Prasek has what they call a "share-the-fare" program. You pay \$39.95 for the first SXR PLUS package, and \$7.50 for each additional package. There is no limit or restriction on the number of additional copies you can purchase at this

price. User manuals alone are now \$6.00.

If you destroy your disk, you can also get a replacement disk for \$5.00 if you return the original disk. While the old warranty was only five days, you are now protected for 90 days. Returns are made to Prasek.

Perfect Writer Perfect Speller Company: Perfect Software, Inc., 1001 Camelia St., Berkeley, CA 94710 Price: \$399.00

Circle Reader Service No. 135
Reviewed by Charles K. Ballinger

By now you probably have read innumerable reviews on the most common word processors currently on the market and still can't decide which one is for you.

Well, perhaps you are going about it the wrong way. As in most things you purchase, don't you decide what you want the product to do and then go out and find something that fits your requirements? (Of course, you know full well that in all things compromises must be made.)

It has been 5 months now since I first received this software for review. I have run it through its paces enough to feel comfortable with it, and I can now tell you what may make you interested in this product.

Since I do a lot of programming in a variety of languages, I look for a word processor that can handle the demands made on it by a special group of users (programmers) and still be used to produce the documentation that is so necessary for an item I would produce. The following are things I wanted in a word processor but had not been able to find in any of the others currently on the market:

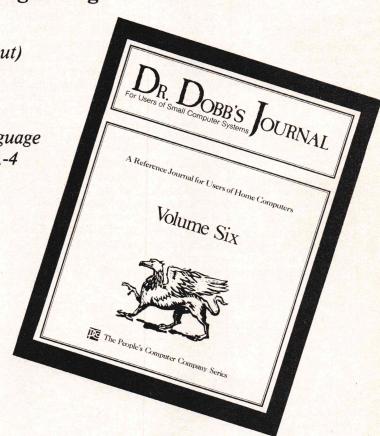
- No need for imbedded special codes that would prevent me from sending text files to other systems.
- Ability to view more than one input file in order to merge more than one program source into a new program.
- The absence of memory restrictions on the size of the source that I could edit at one time.
- 4. The ability to produce documentation that consists of several files and to print them in a contiguous fashion and also produce more than one copy at a time.

I found that Perfect Writer lives up

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to these qualifications and even more.

As supplied (on 8-inch disk), the software was fairly easy to install. Perfect Writer is written in C and, as such, provides the authors a means to continually update the product. Prior to installation you should read the introduction to get an overview of the software. Pay close attention to the information on swapping (this is how Perfect Writer allows files larger than available memory), because this is where your disk capacity may play an important part. After the introduction you can proceed to the installation guide in Appendix A.

The terminal configuration portion is by far the easiest. If you have a KayPro, Apple, Vector, Televideo, IBM PC, Osborne, Heath-Zenith, Columbia, Access Matrix, Seattle Computer, GE Intersil, Superbrain, or NorthStar Advantage it is just a matter of selecting the terminal from a displayed menu. Since I am running a Heath H-8 system, it was a simple matter

to select my terminal type.

If you can't find your terminal type on the menu then you may go through a question/answer session with the program to arrive at the correct terminal configuration. This is one of the first programs I've seen where they make it perfectly clear just what you are supposed to respond with. The questions that require other than a yes or no response tell you explicitly to enter your response in hex or in decimal as required, instead of leaving it up to you to guess whether you enter hex, decimal, octal, or ASCII as a response.

Perfect Software's attention to detail in the installation section was a real treat compared to some I've seen. Software I've installed usually falls down in the printer installation portion: not Perfect Writer, however. If you do not have one of the supported printers (Epson, IDS460, Centronics 737, Centronics PS, Diablo 1610, 1620, 1640, 1650, 630, PS or equivalent, NEC 5510, 5520 or PS), you can go through a question/answer session and define your printer to the program. This procedure is very easy to follow, even for a layperson or first-time user.

The documentation may, at times, seem overwhelming, particularly when you first start to use the software. Your best approach would be to skim the material and then start with the lesson disk to get an actual feel of the programs.

My only minor complaint with the documentation is in the size of the book. I hate having to break the spine of a book just to get the thing to lie flat so I don't keep losing my place (or having to place a weight on it to keep it open). A loose-leaf book, even if in the same size, would have been far easier to use and would have made updates or corrections to the manual possible. How do you correct a manual that is bound, short of making the user purchase an entirely new copy?

The associated fold-out reference card is very handy and goes a long ways in helping you find that command you are looking for. All in all their documentation rates a "well done."

With the inclusion of a separate lesson

disk, it is almost impossible not to gain a working familiarity with Perfect Writer/Speller in a couple of hours. Granted you won't know all the commands or capabilities, but then of what value is a software product that you can take to its outer limits in such a short period of time? I'll tell you, it's probably one that you will replace very soon. Most of the commands have a scheme that is quickly apparent, and you'll find that they are easily memorized. What's nice about Perfect Writer/Speller is that I'm still discovering things that it can do.

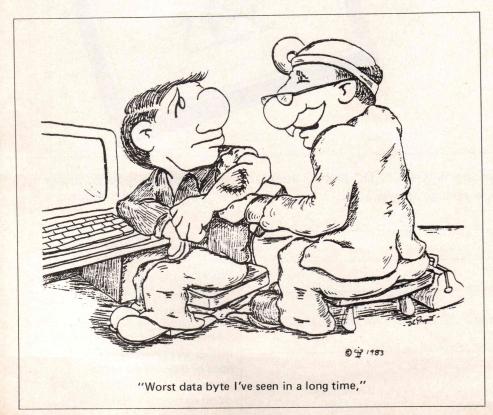
Perfect Speller contains a 50,000 word dictionary which is far easier to use than MicroPro's SpellStar. The speed, at least on my machine (2 MHz 8080 with 8-inch drives), approaches the estimate given in their manual: approximately 4000 words/minute.

I only had one minor problem with Perfect Writer/Speller (my own mistake, caused by being overzealous). When I contacted Perfect Software, Inc. — they even talk to users, unlike some — I found them to be most helpful and willing to provide the assistance I needed, pointing out that I had inadvertently missed a step. Not having identified myself as a software reviewer, I can only conclude that this is the normal support level that all users can expect to receive.

As mentioned earlier, this product is a true help to a programmer when it comes to constructing new programs from existing ones. The ability to have multiple file buffers is ideal from a programming point of view because you can now pull currently running source code into your new creation without undue effort. In fact, you are allowed access to a maximum of seven input documents, and, with the ability to use split-screen mode, new program construction time should be cut by a considerable margin.

All things considered I find this a perfect programmers' tool. It provides you with a product that will assist you from coding through final documentation of your software. Now you only have to learn one product to produce both your source code and your finished documentation, without the fuss and bother of trying to remember more than one set of word processing commands. I'm sure you have had the problem of mixing commands if you currently use more than one word processor or have access to different machines.

I heartily recommend this product as an excellent programming tool and documentation aid. With the introduction of the additional Perfect software family, and the trend of software vendors to provide integrated software, I think this is going to be a hard product to beat.



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BOOK REVIEWS

Cryptography. Proceedings of the Workshop on Cryptography, Burg Feuerstein, Germany, Mar 29-Apr 2, 1982.

Edited by Thomas Beth Published by Springer-Verlag, 1983 \$18.50, 402 pages Reviewed by Richard L. Lozes

A Proceedings volume must be viewed from two perspectives: global and detailed. A smooth, logically connected, and well-styled global view is the responsibility of the editor. An accurate and complete paper is the responsibility of the authors.

These Proceedings cover a wide range of topics from classical cryptography to the Rivest-Shamir-Adleman scheme. Given the strides made in cryptography in the last decade, it is surprising to find papers concerning mechanical contrivances such as the Enigma machine. This historical view, nonetheless, is helpful; it motivates the abstract mathematical arguments later in the volume by means of physical analogies. The editor has performed his job ably.

The authors, too, hold up their end of the bargain. By and large, the papers are well organized and clearly presented. Understandably, one or two authors have difficulty with the English language.

Certainly, a Proceedings volume can never be recommended as a self-study text. However, I suspect that students of cryptography, with little prior exposure, could glean much valuable knowledge, especially if they were careful to study and commit to memory the introductory mathematics to be found in the first paper.

IBM Data Files
By David Miller
Published by Reston Publishing/Prentice-Hall
\$15.00 paper, 260 pages
Reviewed by James Moran*

One of the failures of documentation for personal computers is the paucity of adequate information on the use of disk

files. Documentation for the IBM PC is not an exception, unfortunately, but puzzled PC users might find some needed answers in this book.

In IBM Data Files, author David Miller has created a useful tutorial for novice users of the IBM PC. The book assumes that the typical PC owner's familiarity with disk devices stops with the user's ability to load a piece of packaged software, and it attempts to address that limited knowledge in classic textbook format. Information is presented clearly in a precise manner and is followed by short quizzes to test the reader's understanding. One of the nice touches in the book is the grouping of programming examples at the end of each chapter. After finishing a chapter, the reader keys in the examples and runs them on the PC. In addition to reinforcing the learning process, this concept leaves the reader with a library of practical and usable programs after the book has been read.

IBM Data Files begins with a short introduction on the mechanics of using a diskette drive and progresses to file design, programming techniques, and, finally, file planning strategies for integrated systems—a particularly useful section, if a bit short. VisiCalc users will find the section on creating DIF files to be the most useful part of the book since the author was most generous in furnishing examples on how to standardize user file formats so that they may be transferred for use in a VisiCalc program.

This book does have some minor weak spots, and those would be most noticeable to readers towards whom author Miller seems to have directed his information: the beginning user. For example, it is somewhat surprising not to find a single diagram or graphic to visually enlighten the reader about the physical organization of a diskette. Some examples of random access algorithms would also have made this book more useful to PC owners who have an intermediate level of knowledge about their equipment.

All things considered, the book would probably be most useful to beginning and intermediate users. Those who are more technically knowledgeable may prefer a book with greater depth or more advanced topics although even they might find that the working program examples are worth the price of the book. Among those programs is a complete home inventory system that is contained in 36 pages of BASIC code. And for those readers that

would rather not spend more than a few hours keying in the programs, an offer by the author to supply the programs on diskette for fifteen dollars seems like a pretty good deal.

Microprocessor Support Chips:
Theory, Design, and Applications

By T. J. Byers Published by MicroText/McGraw-Hill,

August 1983 \$38.00 hardcover, 224 pages (170 illustrations)

Reviewed by David W. Carroll

Microprocessor system designers have recognized that in most applications it is neither practical nor efficient to have a microprocessor perform all of the routine tasks required in a complex system. Rather, specialized support integrated circuits (ICs) have been developed to handle individual system support requirements.

Recognizing that these parts are necessary for economical, high performance designs in today's complex microprocessor-based systems, T. J. Byers has compiled a collection of 97 state-of-the-art support chips in his book, Microprocessor Support Chips: Theory, Design, and Applications, recently published by Micro-Text/McGraw-Hill. This design reference offers often hard-to-get information on many support ICs, including pin-outs, typical application schematics, and specific interfacing and design information. This book is not intended to replace the manufacturers' data sheets; rather it supplements them with real-world application information.

Some areas covered include telecommunications, power supply and special purpose, interface, control, video, and A/D and D/A converters. Telecommunications parts include various serial communications interfaces, protocol converters, local area network controllers and interfaces. and modem chips. Under power supply and special purpose we find keyboard and display controllers, data encryption chips, timers, parallel interfaces, and pulse width modulators for switching power supplies. The control ICs section includes printer and stepper motor controllers, floppy disk formatters and controllers, and Winchester hard disk controllers. The video chapter covers most currently popular

^{*}Copyright © 1983 by Compu-Syn.

video display and CRT controller chips. Twelve data converters are included in the last chapter on A/D and D/A converters.

This book is primarily a design reference for serious digital engineers, allowing a rapid comparison of alternative components and helping to rapidly initiate the design of an operating application circuit. Each part is covered on two facing pages - details of operation, pin-out, and application notes are on the left page while the application schematics are on the right. The operating specifications and parameters of parts are not included in this book. Users should plan on obtaining data sheets on parts from their manufacturers. The book's format is hardcover, 8½ x 11 inches, making the pin-outs and schematics easy to read and somewhat justifying its \$38.00 price tag, if one keeps in mind the limited audience and short useful life of this type of subject material.

The author advises that the selection of parts was made based on the current trends in the industry and the publisher's limitation of 100 total parts. A second volume may be expected next year, covering an additional 100 support ICs. grated circuits.

Overall, Microprocessor Support Chips lives up to its author's stated goal to "greatly facilitate the use of new chips in current designs." However, I find its subtitle, Theory, Design, and Applications, misleading. Very little theory is presented and design aid is mostly by example. Perhaps Design Applications would have been a more appropriate subtitle for Microprocessor Support Chips.

Z80 Applications
By James W. Coffron
Published by SYBEX
\$15.95, 295 pages
Reviewed by Chuck Ballinger

As you might have guessed from the title, this book deals with the internal world of the Z80 chip as well as with all of the associated support chips.

The book is easy to follow and presents the concepts and functions of the Z80 in an easy to understand manner. With the aid of schematics and diagrams the author explains many areas of common confusion.

The book first takes you on a tour of the various ROMs (ROM, PROM, EP-ROM, and EAROM) and gives an in-depth explanation of each one in terms of function and application. Continuing on the tour, both static and dynamic RAM are explained, along with a discussion of circumstances where one would be preferred over the other. Complete layouts are also presented for most of the cur-

rently used RAM chips, including wiring hints.

Chapters on Z80 I/O and Interrupts proved very informative. I never really quite understood Interrupts until going through that chapter. The author's blend of excellent text with visual diagrams and circuit layouts presented what I had been unable to find in any previous text.

If you are interested in knowing the how and why of your Z80-based system, you are sure to find an answer in this book. In the chapters dealing with the Z80 PIO (8255), the author has even included assembler source for various routines that are common for the 8255 I/O chip; he steps you through the routines so you can see why they are coded in that fashion. This section also includes an explanation of the Z80 CTC (countertimer-chip), as well as assembler source.

With the current interest in modem communications, the chapter on serial communications may help you explore the hows and whys. Start bits, parity bits, and stop bits are described clearly and thoroughly. For communications

buffs the 8251 USART is covered pin by pin. Examples are presented that even provide information on how to send commands to the command register, which is something I've never seen explained to my satisfaction in any other book.

The book can be used for those interested in designing their own systems. However, I think that the number of homebrew systems being built are going the same direction as homebrew ham equipment — it's cheaper to buy an assembled unit than to build one. Where the book does come through is in the explanation of the various chips that make a Z80-based system and how special features found in most computer systems can be programmed.

The discussions of interrupts and programming the command modes for the serial I/O chips make the book a valuable addition to anyone's library. With the advent of newer CPU's many say the 8080 and Z80 will disappear. To the contrary, I say that you will start seeing the devices in your toasters, blenders, etc., and perhaps you would like to know what makes

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Dr. Dobb's Journal March 1984

them tick. This book can be used both as a reference manual and a repair manual since it gives you an in-depth working of what goes on under your CPU's cover.

Concepts for Distributed Systems Design By Gregor von Bochmann Published by Springer-Verlag, BerlinHeidelberg \$19.00, 259 pages Reviewed by Robert Irving

Von Bochmann uses very formal guidelines when writing about systems. A full half of the book, including the annexes, is devoted to the requirements for specification of a distributed system. The first quarter defines and gives examples of distributed systems, specifically excluding multiprocessor systems but including a modular system in one physical location, which is laid out so that it could be distributed. The second quarter reviews architecture and communications protocols.

The reader is assumed to be a computer professional well-versed in conventional computer architecture and modular software design. This approach, combined with the terse, formal text and lack of an index, limits the application of this book as a text as well as a reference volume. However, if you already have a copy of Paul Green's Computer Network Architectures and Protocols (Plenum 1982), this book would be a useful supplement with regard to writing system specifications.

An Introduction to Numerical Methods with Pascal By L. V. Atkinson and P. J. Harley

Published by Addison-Wesley
Publishing Company
\$16.95, 300 pages
Reviewed by Robert Ashworth

This book blends numerical methods with Pascal and calls upon the benefits of those data types to provide a natural implementation of the respective methods. While it is a college text for those of a scientific bent, no knowledge of numerical analysis is required. Familiarity with Pascal is assumed, and the text flows very well and can quickly be adapted for micros or even mainframe environments.

The text begins with a review of Pascal and builds on its merits: transparency (the intention of a well-written program being self-evident), security (the completing to detect errors), and efficien.

(its design taking implementation into account). Emphasis is on the implementation of methods on a computer so as to reinforce their understanding.

Next comes a full treatment of rounding errors: those contributed by the method selected, and those inherent in the computer itself. Chapters 3-5 cover the solution of linear and nonlinear equations. Here the LU decomposition techniques are given prominence and the inverse of a matrix is explained. This part concludes with the treatment of the eigenvalue and the eigenvector problems, as well as a short discussion of the non-symmetrical cases.

The next three chapters deal with discrete functions, differentiation and integration, and finally present an overview of ordinary differential equations. The Pascal programs are written out in full, extending over several pages.

Exercises were provided for each chapter but a solutions appendix was not included. I found the text to be very well written, with sixty-two useful programs.

is a collection of four papers, and each paper has little cross-relationship with the other, you must understand the material from the onset in order to have any inkling of what the individual authors are trying to convey.

If you are involved in the design of compilers, interpreters, or editors then this book may be of value to you. The textbook value may justify the cost as the areas covered include, "The Planes Interpreter and Compiler for ATN Grammars," "An ATN Programming Environment," "Compiling ATN into MacLisp," and finally "Towards the Elastic ATN Implementation." If one of these individual papers is of interest to you then you might be interested in this book. For the vast majority of computer users/programmers this book should be bypassed. For those interested, the book is full of examples of the various methods of ATN. The entire text is double-spaced in typical college manuscript format and is well structured.

DDJ

Augmented Transition Networks Edited by Leonard Bolc Published by Springer-Verlag, 1983 \$29.00, 213 pages Reviewed by Chuck Ballinger

Confused by the title? Well, if you are not sure what an Augmented Transition Network is I'll try to explain. An Augmented Transition Network (ATN) is used in the design of interpreters, compilers, and editors as a method of factoring the input. Assume you have a data base that contains information on airplanes and you have presented a computer terminal with the following question: "How many Skyhawks required engine repair in 1973?" The processing of that request would require three main steps: parsing, interpretation, and evaluation. The first phase is what this book, as a collection of four papers, attempts to

As a collection of papers that are related, but not directly integrated, there is some discord in the authors' presentations. Due to the nature of the subject, the amount of deviation does not detract from the initial subject, provided you are well versed in ATNs before picking up this book. This is not a tutorial or step-by-step book. If you don't know ATNs at the onset this book will not help you pick up any additional information.

Unless you are well versed in compiler design and implementation this book will be far above the average- or medium-level programmer. This text is intended for a very limited audience with a very specialized background or interest level. Since it

Letters

(Continued from page 7)

Fast Conic Curves

Dear DDJ:

A general algorithm for drawing any conic curve at any orientation uses a slightly different viewpoint than the one detailed by Michael Enright (DDJ No. 86, December 1983, pp.19-20), but still yields all the benefits of his method. Assuming the cursor starts on the curve, the best direction of the four possible directions is chosen by evaluating the potential distance that each would be from the actual curve and taking the one that gives the smallest. This distance is found by using a form of the gradient which for conic equations is linear, i.e., using addition not multiplication. By updating a table of possible changes again using only addition, the gradient at each point can be maintained. The algorithm continues in this way until it nears the endpoint. The algorithm draws the "best possible" curve the one closest to the actual curve.

I developed this method three years ago for drawing circles and circular arcs. In the degenerate case it yields the standard method for drawing lines. With the growing speed and capacities of microprocessors, I believe that algorithms that "look before they leap" will be increasingly useful.

Jim Hatton 3715 Summit Drive Mt. Shasta, CA 96067

DDJ



For only \$95, Q/C is a ready-to-use C compiler for CP/M. You get complete source code for the compiler and over 75 library functions. Q/C is upward compatible with UNIX Version 7 C, but doesn't support long integers, float, parameterized #defines, and bit fields.

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Version 3.2 of Q/C has many new features: structure initialization, faster runtime routines, faster compilation, and improved ROM support. Yes, Q/C has casts, typedef, sizeof, and function typing. The Q/C User's Manual is available for \$20 (applies toward purchase). VISA and MasterCard welcome.



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16-BIT SOFTWARE TOOLBOX

by Ray Duncan

Bill Savage's floating-point benchmark program, discussed in this column in DDJ, September 1983, apparently caught the interest of a large number of readers. Versions in BASIC (9/83), PL/I (9/83), Fortran (1/84), Forth (11/83), Pascal (11/83), and C (Listing, page 96) have been contributed along with many timing reports.

In Table I (page 93) I have compiled the figures sent to me to date and sorted them in order of execution time (thank goodness for dBASE II). When duplicate timings on the same language and CPU were sent by different readers, I generally picked the more conservative results. A few languages were omitted altogether because of gross conflicts between two different reports (the IBM 4341 Fortran was one of these). I have also attempted to screen out any obvious typographical errors or blatant publicity grabs; nevertheless, let the reader beware!

The largest number of contributions came to *DDJ* via Gerald Edgar of Columbus, Ohio, who put the original benchmark program onto the CompuServe bulletin board system and asked for donations. This resulted in a set of over 50 timings, which he then sorted by degree of error, CPU, and language. After eliminating the duplications, I added these to my own table and designated them by the name "COMPUSERVE" in the "Source" column. Hearty thanks to all the anonymous CompuServe users who took the time to read and comment on this project.

Turning our attention to the table, the column "FPP" contains the type of hardware floating-point support that was used, if any was noted by the contributor. Most of the timings were accurate only to the nearest second (except for the very fast mainframes); they are displayed to three decimal places as an artifact of the dBASE II report program. The column "Error" was calculated as ABS (2500.000 – A), converting to scientific notation and rounding the mantissa to the nearest integer. Obviously, the smallest errors correspond to the most accurate results.

This collection demonstrates very clearly that personal computers based on the Intel 8086 or 8088, when augmented with the 8087 floating-point coprocessor, can offer performance on this limited benchmark that is competitive with "super-minis" or mainframe computers costing ten to a hundred times more. Of

course, many significant issues such as the cost, size, and speed of mass storage, sophistication of systems tools, and ability to handle large numbers of interdependent processes are being totally ignored here.

We also received some advice and gentle chastisement from readers who are more knowledgeable on the subject of floating-point libraries and benchmarks than yours truly. I have excerpted a few of these letters below.

Jeffrey M. Speiser, of San Diego, California, writes: "It should be noted that for this benchmark the accuracy of the result is only suggestive of the accuracy of the floating-point arithmetic since:

- "1) Errors of opposite signs can cancel in the final result, suggesting greater than actual accuracy.
- "2) The errors can occur primarily over a small part of the argument range, suggesting much worse accuracy than is present over most of the argument range.
- "3) Careful examination of one case showed that not only was the error primarily in the TAN-ARCTAN pair, but it could be explained by assuming that both functions were computed exactly, with roundings only to the nearest machine-representable number. Moral: do a simple sensitivity analysis for any critical calculation, by using the first two terms in a Taylor series expansion."

Harry J. Smith, of Saratoga, California, writes: "...I duplicated [the previously published results] for Fortran-80, BASIC-80, and PL/I-80 with the standard floating-point libraries. But when I also displayed the results of each iteration. I made a startling discovery. Both the Fortran-80 and BASIC-80 programs reached their final value of 2304.863 and 2304.860, respectively, on the 2230th iteration and then repeated this same output for the next 270 iterations. If the original program had been asked to do 2305 iterations instead of 2500, then Fortran-80 and BASIC-80 would have looked quite good. The foregoing exemplifies the possibility of superficial tests leading to false conclusions..." He went on to describe in detail some procedures for evaluating

error in floating-point libraries, which we will save for a later column.

Karl J. Casper, Professor of Physics at Cleveland State University, contributed timings on machines ranging from the IBM 370 to the Hewlett-Packard 15C calculator. He commented: "This program . . . is somewhat unfair to the microcomputer if only single-precision numbers and functions are used. For example, I do not think that Microsoft really improved their algorithms from the Model I TRS-80 to the Color Computer. It is more probable that the increased accuracy results from increasing single precision to nine places on the CoCo.

"The problem is that the program begins to check angles very close to 90 degrees after only a few iterations. Most rational number approximations are designed to give good values for the functions over a wide range of arguments. It is certainly not clear that this program would correspond to any physically meaningful problems. Where physicists find that they are dealing with very small numbers, they would not usually try to evaluate these numbers by adding and subtracting large numbers. Physically meaningful benchmarks, however, would be very useful for evaluating computers.

"The point I am making is pretty simple. The Model I TRS-80 fails to get an accurate value simply because it lacks double-precision functions in BASIC. Both Pascal-80 and Fortran-80 (using double-precision functions) generate the correct value when run on the Model I, whereas Molinerx 5.1 Pascal generates the same value as Level II BASIC. (It is, of course, comforting to find that both the Sinclair ZX-81 and the HP-15C calculator obtain accurate values.) For a scientist, speed is occasionally important, but the fact that some computers run at breakneck speed is hardly significant when the wrong answer is obtained. Checking this benchmark on the IBM 370 yielded even faster speeds than MicroFloat [hardware-assisted floating-point libraries]. But while an interactive Waterloo BASIC obtained the correct answer, IBM BASIC failed by a wider margin than any microcomputer."

I will continue to add results to the data base as they come in and will plan to publish the expanded collection in about six months. If any readers have proposals for a more fair, exhaustive, and physically

meaningful floating-point benchmark program, let's see those too! We have a noticeable deficiency of C language results compared to the huge number of compilers that are currently available. Some inter-

esting languages are missing altogether, including LISP, Logo, Ada, and Modula II. If you are kind enough to send in timing results, be sure to note whether single- or double-precision were used and the type

of hardware floating-point processor (if any).

Reader Ballot

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	Table 1.
Floating-Poi	nt Benchmarks for DDJ

COMPUTER	MHZ	LANGUAGE	VERS	FPP	TIME (SEC)	ERROR	SOURCE	COMMENTS
HONEYWELL		MULTICS FORTRAN			0.500	4E+1	SHERMAN GROMME	SINGLE PREC, TIME APPROX
IBM 370		WATERLOO BASIC			A FTA	DE A	Want mamma	
DEC VAX 11/780		VMS FORTRAN-77			0.578	<1E-3	SHERMAN GROMME	DOUBLE PREC, TIME APPROX DOUBLE PRECISION PRESUMED SINGLE PREC
HP 1000-F		FORTRAN 4	2040		0.630	2E+2	COMPUSERVE	
IBM 3081		PL/I MULTICS FORTRAN			0.660	2E-25	COMPUSERVE	
HONEYWELL		MULTICS FORTRAN			1.000	1E-3	SHERMAN GRONME	DOUBLE PREC. TIME APPROX
DEC VAX 11/780		VMS FORTRAN-77			1.054	(1E-3	SHERMAN GROMME	DOUBLE PRECISION
IBM 370		IBM BASIC			1.530	2E+3	KARL CASPER	PRESUMED SINGLE PREC
HP A700		FORTRAN 4			2.000	3E-8	COMPUSERVE	
IBM FC (8088)	4.77	WL SYSTEMS FORTH		8087	3.200	1E-3	JOHN GOTWALS	
8086	5.0	FORTRAN 4 WL SYSTEMS FORTH PL/I-86 FORTRAN (UNIX)	1.01	8087	3.700	2E+1	BILL SAVAGE	MICROFLOAT LIBRARY
HP 9000 (68000)		FORTRAN (UNIX)			4,000	2E-6	COMPLISERVE	Manufichi Libinii
HP 1000-F		FORTRAN FTN4	2040		4.000	5E-3	COMPLISERVE	.
8086	5.0	PL/I-86	1.01	8087	4.000	2E+1	COMPUSERVE	
IBM PC (8088)	4.77	MVP-FORTH		8087	4,300	(1E-3	C. SPRINGER	
DEC PDP 11/44		RSX-11M FORTRAN		FIS	4,500	2E+2	JOHN TOSCANO	
DEC VAX 11/780		PASCAL	2.1		5.000	6F-10	COMPLISEBAE	
IBM PC (8088)	4.77	MICROSOFT PASCAL	3.11	8087	6.000	(1F-3	J. SPEISER	NRI PREC CENTILE OF LID
IBM PC (8088)	4.77	MICROSOFT FORTRAN	3.1	8087	6.200	(1F-3	J SPEISER	BBI PDEC CENTILE OF LIB
DEC PDP 11/44		RSX-11M FORTRAN		FIS	4.900	7F-1	JOHN TOSCANO	DULLINEO, SENTILE DI.LIB
DEC PDP 11/34		RT-11 FORTRAN		FIS	7.500		1 SPEISED	DORDEE LUEC
IBM PC (8088)	4.77	BASIC COMPILER	1.00	8087	7.800	1E-3	J. SPEISER	BRI PREC CEATTIC OT LID
HP 1000F		BASIC			8.000	3F+2	CUMPHICE OF	DEL FREE, SERVICE BY.LIB
8085	5.0	RNAC		8232	10.200	5E-3	RILL SAVAGE	MICROFI DAT I IRRARY
8085	5.0	PL/I-80	1.40	8232	10.400	5F-3	RIII SAVAGE	MICROFLONT LIBRARY
8085	5.0	BASIC-80	5.20	8232	10.700	(1F-3	RILL SAVAGE	MICROFLONT LIBRARY
8085	5.0	FORTRAN-80	3.40	8232	12,500	5F-3	BILL SAVAGE	MICROS CON CIBRANI
IBM PC (8088)	4.77	BASIC COMPILER	1.00	8087	14.700	CIF-3	J SPEISER	THE PREC MICROWARE LIE
6502	4.0	UCSD P SYSTEM	II	8231	15.500	2F+2	STEVEN SPEARS	DEL THEE, HICHEMANE LIE
8088	4.7	UCSD PASCAL	IV.1	8087	18.000	2F-7	COMPUSERVE	
APPLE II (6502)	1.75	MVP-FORTH		9511	18.500	2F+2	C SPRINGER	
DEC PDP 11/44		RSX-11M BASIC-PLUS-2			21.800	1E+2	חאמינות שוחנ	STNELE POEC
DEC MINC 11/23		FORTRAN		FPU	22.000	2E+2	BILL SAVAGE	DBL PREC, SEATTLE 87.LIB DBL PREC, SEATTLE 87.LIB DOUBLE PREC DBL PREC, SEATTLE 87.LIB MICROFLOAT LIBRARY MICROFLOAT LIBRARY MICROFLOAT LIBRARY MICROFLOAT LIBRARY MICROFLOAT LIBRARY DBL PREC, MICROWARE LIB SINGLE PREC
DEC PDP 11/44		RSX-11M BASIC			27.200	1F+2	JOHN TOSCAND	
HEATH H-89 (Z-80)	4.0	FORTRAN-80		9511	31.000		JOHN TOSCANO	
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1000	(Table I continued)								
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DOUBLE PREC 1.338				1.07					SINGLE PREC
BASIC BASI				1.33D	8087				DOUBLE PREC
DEC PR 11/14 RSX11M FORTERN 103.000 ZE-1 JOHN TOSCAND DOUBLE PREC							3E+2	BILL SAVAGE	
BEC PRP 11/44 RSX-11M BASIC-PLUS-2						103.000	2E-1	JOHN TOSCANO	DOUBLE PREC
Part						113.600	<1E-3	JOHN TOSCANO	DOUBLE PREC
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March Marc						117.000	1E-8	J. SPEISER	
P 1000E						119.000	8E+0	ANDY BALL	MICROWARE SYSTEMS CORP
SOURCE S.0 FORTRAN-80 S.40 140,800 2E+2 BILL SAVAGE SOURCE				2026		121.000	3E-8	COMPUSERVE	
Series S		5.0		3.40		140.800	2E+2	BILL SAVAGE	
Part						140.800	2E+2		
Tentity Tent						140.800	1E-3	J. SPEISER	
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SSS FORTRAN 1,04		2.0				149.000	2E-2	ANDY BALL	MICROWARE SYSTEMS CORP
IBM PC (8088)				1.04		154.000	2E-7	COMPUSERVE	
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Z-80								BILL SAVAGE	
HEATH H-B9 (Z-B0) 4.0 MRASIC COMPILER 5.2 189.700 2E+2 JOHN TOSCAND HEATH H-B9 (Z-B0) 4.0 FORTRAN-B0 203.000 2E+2 JOHN TOSCAND TANDY 16B (68000) XENIX CC COMPILER 211.000 1E-3 JOHN TOSCAND JEM PC (808B) 4.77 SUPERSOFT FORTRAN 1.07 211.000 (1E-3 HUGH KANABATA DOUBLE PREC 8000 6.0 C (TRS-XENIX) 212.000 1E-6 COMPUSERVE JEM PC (808B) 4.77 DRI PERSONAL BASIC 1.0 217.000 1E+3 RAY DUNCAN SINGLE PREC HEATH H-B9 (Z-B0) 4.0 MBASIC 5.22 233.740 2E+2 JOHN TOSCAND DEC PDP B BOS								COMPUSERVE	
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 Algorithms B-Tree ISAM Concepts CP/M BDOS and BIOS Calls for C Serial Expansion in Forth Fast Matrix
 Operations in Forth, Part I Yes, You
 Can Trace Through BDOS Julian Dates
 for Microcomputers 8088 Addressing
 Modes 8088 Line Generator CP/M
 Plus.
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 Sizing Memory on the IBM PC.
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 Symbols and I/O Benchmarks for CP/M
 Plus CP/M Plus Memory Management
 Zero Length File Test PAUSEIF,
 QUITIF, and now SKIPIF ACTXX
 Cross Assemblers.

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 Precompiled Forth Modules Signed
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 Standards The Forth Sort A speed
 and accuracy benchmark program for
 high-level languages Using a Digital
 Spreadsheet Program for Something Fun
 and Unusual.
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(Table I continued)								
COMPUTER	MHZ	LANGUAGE	VERS	FPP	TIME (SEC)	ERROR	SOURCE	COMMENTS
TANDY I (Z-80)		BASIC			512.000	1E+2	KARL CASPER	
COMMODORE 64 (6510)		BASIC			514.000	<1E-3	TERRY THOMAS	
Z-80	4.0	ZBAS	1.4		540.000	2E+2	COMPUSERVE	
TANDY CC (6809)		FORTH			560.000	<1E-3	GARY BERGSTROM	
TANDY CC (6809)		BASIC RS-CC	1.0		585.000	<1E-3	GARY BERGSTROM	
ZENITH ZW110 8088	5.0	CI C86			655.000	<1E-3	A. F. HERBST	
IBM PC (8088)	4.77	CI C86	1.330		695.000	1E-3	J. SPEISER	DOUBLE PREC
TANDY 16B (68000)		XENIX MBASIC			773.600	1E-3	JOHN TOSCANO	
IBM PC (8088)	4.77	BASIC INTERPRETER	2.0		890.000	1E-3	J. SPEISER	DOUBLE PREC
Z-80	2.0	NEVADA FORTRAN	3.0		975.000	8E+1	SHERMAN GROMME	
8085	6.0	CB-80	1.2		997.000	2E+1	COMPUSERVE	
8085	6.0	FORTRAN-80	3.4		1251.000	1E-12	COMPUSERVE	
8085	6.0	CBASIC	2.06		1623.000	2E+1	COMPUSERVE	
7-80	4.0	BASIC-80	5.3		1980.000	1E-7	COMPUSERVE	
Z-80	4.0	AZTEC C II	1.05		2190.000	1E-5	COMPUSERVE	
HEATH H-89 (Z-80)	4.0	AZTEC C	1.05		2226.000	(1E-3	JOHN TOSCAND	
TI-59 CALCULATOR					3240.000	7E-3	COMPUSERVE	
7-80	2.5	S-BASIC			3900.000) 3E+4	COMPUSERVE	
TANDY I (Z-80)		FORTRAN-80			4602.000	(1E-3	KARL CASPER	DOUBLE PREC
TANDY I (Z-80)		PASCAL-80			5220.000	(1E-3	KARL CASPER	
7-80	4.0	JRT PASCAL	2.1		5760.000	1E-3	COMPUSERVE	
HP-65 CALCULATOR					6000.00	9E-1	COMPUSERVE	
HP 15C CALCULATOR					10200.000	1E-2	KARL CASPER	
SINCLAIR ZX-81		BASIC			86400.00	0 3E-1	KARL CASPER	ABOUT 1 DAY
								End Table

16-Bit Toolbox Listing (Text begins on page 92)

```
main()
/* Floating point accuracy test adapted from DDJ 9/83 p. 122 */
{
  int i,iloop=2500;
  double a=1;
  double tan(),atan(),exp(),log(),sqrt();

  for (i=1;i<iloop;i++)
    {
     a=tan(atan(exp(log(sqrt(a*a)))))+1.0;
    }
  printf("\na = %10.4f\n",a);
}</pre>
```

End Listing

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OF INTEREST

by Michael Wiesenberg

Faster than a Speeding 8088

Lightning 286, from Lomas Data Products, is an S100 80286 board that runs 8088 and 8086 software four times faster than a 5 MHz 8086 (which itself is considerably faster than the 8088), and can run the 80287 high-speed numeric processor, giving program execution in some programs a doubling of speed. The Lightning 286 can run at clock rates up to 10 MHz, and Lomas suggests that they will increase their rates as Intel improves the 80286 (which currently runs at only 6 MHz). Lightning 286 allows 16 Mb physical address space and 1 Gb virtual address space. Four operating systems, MS-DOS, CP/M-86, Concurrent CP/M-86, and MP/M-86, are currently available with the board, which conforms to IEEE 696 S100 bus specifications. The board costs \$1395, with optional 80287 for another \$550. Reader Service No. 101.

High Speed Modems

Prentice Corporation's limited distance, ALD/1, full and half duplex, asynchronous 1200 to 9600 baud modem conforms to Bell specifications 43401 and 41028 with metallic, twisted-pair Local Area Data Service (LADS) for local office and campus networks. It comes in a stand-alone version for \$300 and rack-mountable for \$200. The HSLD/1, 2400 to 19.200 baud, is \$490 and \$390. The synchronous leased line 4800 baud 208A/B is \$1750 and \$1650, while the 9600 baud 9629 for four-wire 3002-type unconditioned leased lines in point-to-point applications is \$2750 and \$2650. Reader Service No. 107.

Here Come the Little Guys

Even though I rarely mention products for which the press release lists no price, I'd like to describe this one because it shows what you can shortly expect many computers to look like. The **UDI-500**, from Universal Data Incorporated, is the first portable computer with internal batteries and two disk drives. At 11 by

·13 by 3 1/8 inches and 12.8 pounds, it is less than half the size and weight of competitors. The CMOS Z80 permits up to 12 hours of operation on one charge of the nickel-cadmium batteries. You can get 80 hours if you never use the disks or two with constant disk use. The batteries charge fully in 10 hours. The LCD has eight lines of 40 characters. An accessory slot accommodates the optional 300 or 1200 baud direct-connect modem. The low-power static CMOS memory is 64K standard, expandable to 256K. Two plug-in CPU boards (both CMOS) come with the computer, Z80 and 1805. You get CP/M 2.2 and Micro-DOS. Available software includes text processor, spelling checker, data filing system, spread sheet, communications package, and various flavors of BASIC. Reader Service No. 103.

Computers Talking to Each Other

SOFTCOM Telecommunications Utility, from the Software Store, is an intelligent terminal program and CP/M to CP/M file transfer utility that supports XON/XOFF and transmits at up to 9600 baud in full or half duplex. It runs on 8080, 8085, and Z80 systems with at least 32k and CP/M, and costs \$150, Reader Service No. 109.

CP/M Plus for Cromemco

Super BIOS Plus, from MICAH (Micro Applications and Hardware), is CP/M Plus for Cromemco computers, starting at \$495. You get source code for all modules. You can also add the Expand program, a CDOS emulator, and the Select Word Processor. MICAH also carries Morrow hardware configured for Cromemco. Reader Service No. 115.

Don't Give Me Any Static

Staticide Wipes, from ACL Incorporated, are individually packaged towelettes for cleaning and static control of video screens, packed in square

foil packs, 24 to a box, \$4.98 per box. ACL offers free samples and information on other Staticide products. Reader Service Service No. 119.

Come Forth

The Institute for Applied Forth Research is hosting the 1984 Rochester Forth Applications Conference June 5 to 9 at the University of Rochester. Speakers will discuss real-time systems. The conference sponsors are calling for papers on Forth applications and techniques. A 200-word abstract is due April 1 and the paper should be in by May 1. Papers should be on realtime software (including process control, data acquisition, smart instrumentation, laboratory systems, robotics, computer vision, spacecraft navigation, music and voice synthesis), applications (in particular, Forth microchip applications), or Forth technology (finite state machines, control and data structures, and hybrid hardware and software systems). Papers should be sent to Diane Ranocchia (who can be contacted for more information), Institute for Applied Forth Research, Inc., 70 Elmwood Avenue, Rochester, New York 14611; (716) 235-0168.

Proportional Spacing on Wordstar

Proportional Spacing on WordStar, from the Writing Consultants, is a \$20 book that tells you how to do what (they claim) "the industry has said WordStar needs," plus print two or more justified columns on a page and underline_spaces_between_words. Reader Service No. 117.

UNIX for the People

Will UNIX be on microcomputers? Real UNIX? The Office UNIX System from Unisource is not a lookalike, but full UNIX, licensed from AT&T, and developed as VENIX/86 by Ventur-COM for the IBM PC. Unisource's Of-

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SIZE	ERA	SEARCH	WRITE	RECLAIM
XUSER	TYPEX	?	DUMPX	DS
SETRO	CHECK	COPY	FILL	READ
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plications, starting at \$1125. Reader Service No. 111.

Fortran 77

Those of you who use Fortran 66 or, heaven forbid, Fortran 2 and are tired of the gibes from your friends who use Pascal ("Nyah, nyah, you don't have good string-handling capabilities." "Your language ain't structured.") will be thrilled to learn that Absoft has FORTRAN 77 for the Whitesmiths Idris operating system

on 68000 systems. This is an ANSI Fortran 77 compiler that generates position-independent and reentrant object code, supports virtual arrays, has a full screen source level symbolic debugger, and enables dynamic linking of modules at run time. It compiles up to 3500 lines of code per minute and has no restrictions on code size. You'll pay \$695 to \$2000. Reader Service No. 113.

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And a Small Powerhouse

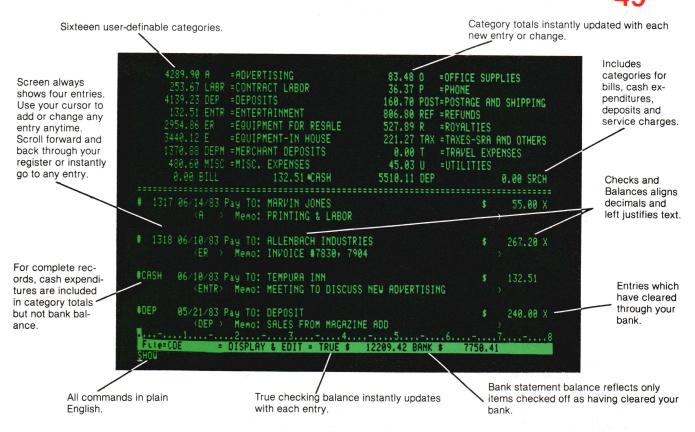
How about a portable computer with a 132-column screen? The Execuport XL from Computer Transceiver Systems Inc. is just that, with Z80 and CP/M, while the Execuport XL+ has both the Z80 and 80186 (true 16-bit) and MS-DOS. Both come bundled with Perfect Calc, Speller,

Writer, and Filer. Optionally, you can get OASIS, CP/NOS, CP/NET, and MP/M. The XL has 80K, while the XL+ has 128K, which can be expanded. Both have a 9-by-5 green phosphor screen with 132 columns by 24 lines, two double-sized, double density 5¼-inch drives with 800K storage

each, and 22 user-programmable function keys. The XL is \$2695 and the XL+ \$3495. You can lease them for, respectively, \$98 and \$130 per month. The computers are supported by CTSI's nationwide on-site sales and service programs. Reader Service No. 105.

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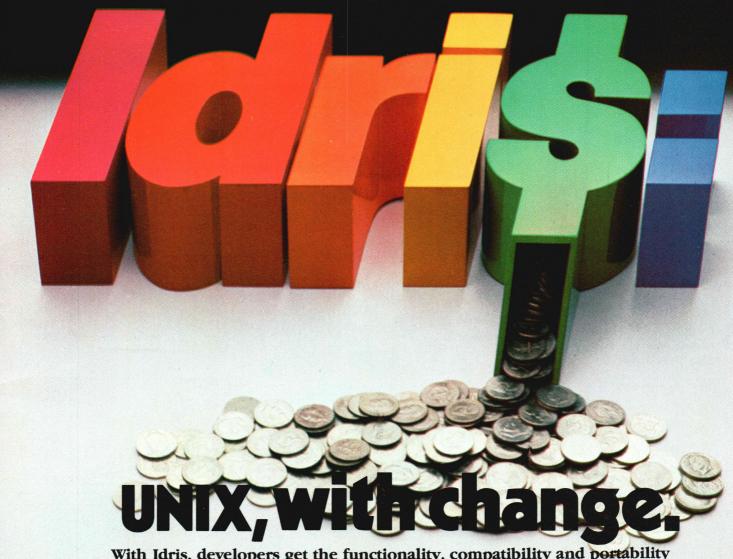
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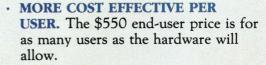
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